

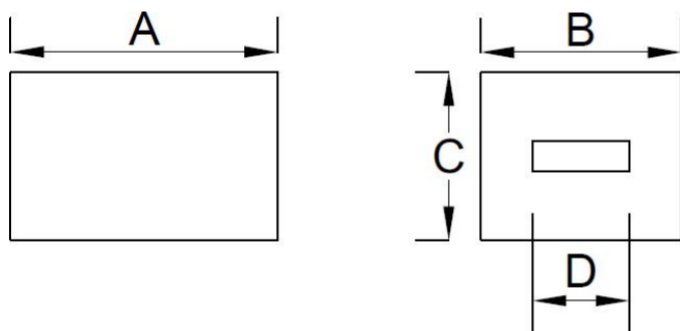
High Current Chip Perforation Bead

◆ PRODUCT IDENTIFICATION

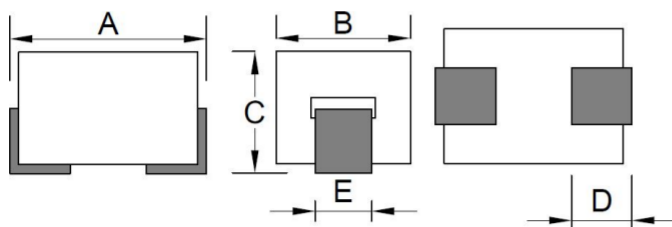
SBH 8532 S 750 N T T
(1) (2) (3) (4) (5) (6) (7)

- 1: Series Type
- 2: Chip Size (mm): Length X Width
- 3: Material Code
- 4: Nominal Impedance: 750=75 Ω
- 5: Impedance Tolerance: N= $\pm 25\%$
- 6: Company Code
- 7: Packaging: Tape Carrier Package

◆ DIMENSION (: mm)



CORE SIZE			
A	B	C	D
8.50 ± 0.25	3.10 ± 0.15	2.50 ± 0.15	1.50 ± 0.15

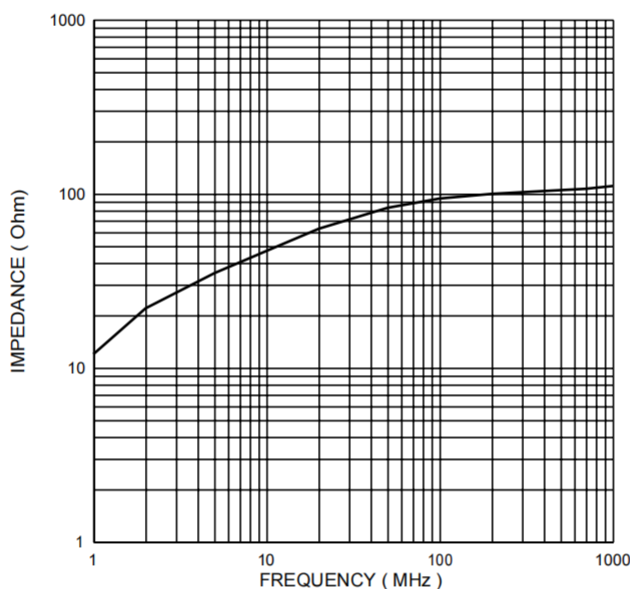


PRODUCT SIZE				
A	B	C	D	E
8.6-9.4	3.1 ± 0.2	2.5 -3.2	1.50 ± 0.50	1.25 ± 0.20

◆ ELECTRICAL SPECIFICATION

Part Number	ELECTRICAL REQUIREMENTS 1			ELECTRICAL REQUIREMENTS 2			DCR (mΩ) Max.	Rated Current	
	Impedance (Ω)	Tolerance	Test Frequency (MHz)	Impedance (Ω)	Tolerance (%)	Test Frequency (MHz)		$\Delta T=40^{\circ}\text{C}$ TYP.	Test Frequency (MHz)
SBH8532S750NTT	45	min	25	75	min	100	1.00	13.0A	1

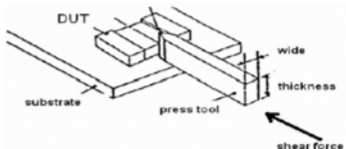
Typical Impedance v.s. Frequency Curve



◆ RELIABILITY AND TEST CONDITION

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	-40~+125°C (on board)	
Electrical Performance Test		
Z(Impedance)	Refer to standard electrical characteristics list.	CH3302,CH1320,CHA113009,Agilent E4991 A
DCR		,Agilent 16197A LCR Meter.
		CH16502,Agilent33420A Micro-Ohm Meter.
Heat Rated Current (I _{rms})	Approximately $\Delta T \leq 40^{\circ}\text{C}$	Heat Rated Current (I _{rms}) will cause the coil temperature rise $\Delta T(^{\circ}\text{C})$ without core loss. 1.Applied the allowed DC current(keep 1 min.). 2.Temperature measured by digital surface thermometer

Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature : $125\pm 2^{\circ}\text{C}$ (Inductor) Applied current : rated current Duration : 1000 $\pm$ 12hrs Measured at room temperature after placing for 24 $\pm$ 2 hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) Humidity : 85 $\pm$ 2R.H, Temperature : $85^{\circ}\text{C} \pm 2$ Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24 $\pm$ 2 hrs
Moisture Resistance	Appearance : No damage. Inductance : within $\pm$ 10% of initial value Q : Shall not exceed the specification value. RDC : within $\pm$ 15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) 1. Baked at 50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to $65^{\circ}\text{C} \pm 2^{\circ}\text{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs, keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs
Thermal shock		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) Condition for 1 cycle Step1 : $-40\pm 2^{\circ}\text{C}$ 30 $\pm$ 5min Step2 : $25\pm 2^{\circ}\text{C}$ $\leq$ 0.5min Step3 : $125\pm 2^{\circ}\text{C}$ 30 $\pm$ 5min Number of cycles : 500 Measured at room temperature after placing for 24 $\pm$ 2 hrs
Vibration		Oscillation Frequency: 10 ~ 2K ~ 10Hz for 20 minutes Equipment : Vibration checker Total Amplitude: $1.52\text{mm} \pm 10\%$ Testing Time : 12 hours (20 minutes, 12 cycles each of 3 orientations).

Item	Performance	Test Condition															
Shock	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value.	<table><tr><th>Type</th><th>Peak value (g/s)</th><th>Normal duration (D) (ms)</th><th>Wave form</th><th>Velocity change (V)ft/sec</th></tr><tr><td>SMD</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr><tr><td>Lead</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr></table> shocks in each direction along 3 perpendicular axes.	Type	Peak value (g/s)	Normal duration (D) (ms)	Wave form	Velocity change (V)ft/sec	SMD	50	11	Half-sine	11.3	Lead	50	11	Half-sine	11.3
Type	Peak value (g/s)	Normal duration (D) (ms)	Wave form	Velocity change (V)ft/sec													
SMD	50	11	Half-sine	11.3													
Lead	50	11	Half-sine	11.3													
Bending	RDC : within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805:40x100x1.2mm <0805:40x100x0.8mm Bending depth: >=0805inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.															
Soderability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150℃,60sec. Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5℃ Flux for lead free: Rosin. 9.5%. Dip time: 4±1sec. Depth: completely cover the termination															
Resistance to Soldering Heat		Number of heat cycles: 1 <table><tr><th>Temperature (℃)</th><th>Time(s)</th><th>Temperature ramp/immersion and emersion rate</th></tr><tr><td>260 ±5(solder temp)</td><td>10 ±1</td><td>25mm/s ±6 mm/s</td></tr></table>	Temperature (℃)	Time(s)	Temperature ramp/immersion and emersion rate	260 ±5(solder temp)	10 ±1	25mm/s ±6 mm/s									
Temperature (℃)	Time(s)	Temperature ramp/immersion and emersion rate															
260 ±5(solder temp)	10 ±1	25mm/s ±6 mm/s															
Terminal Strength	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning:Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force (>0805 inch(2012mm):1kg , <=0805 inch(2012mm):0.5kg) to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 															

◆ SOLDERING AND MOUNTING

Soldering

Mildly activated rosin fluxes are preferred. SBH8532S750NTT terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

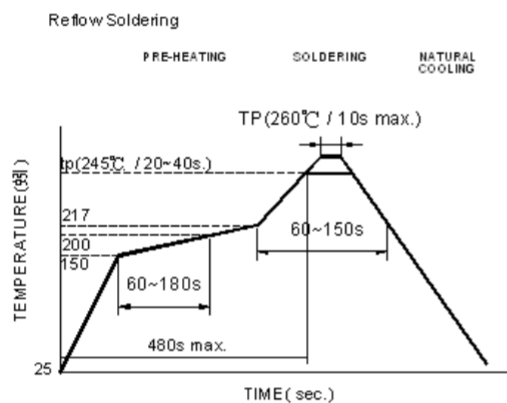
Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

Soldering Iron(Figure 2):

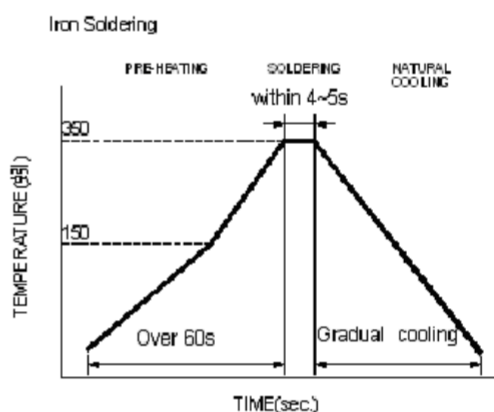
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150°C • 355°C tip temperature (max)
- Never contact the ceramic with the iron tip • 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm • Limit soldering time to 4~5 sec.



Reflow times: 3 times max.

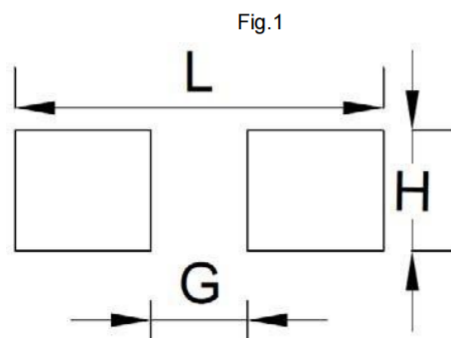
Fig.1



Iron Soldering times: 1 times max.

Fig.2

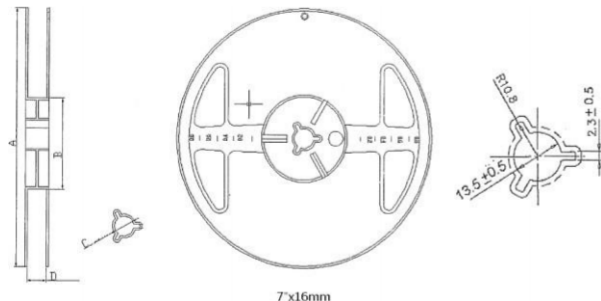
Recommended PC Board Pattern



L(mm)	G(mm)	H(mm)
10.7	4.5	1.5

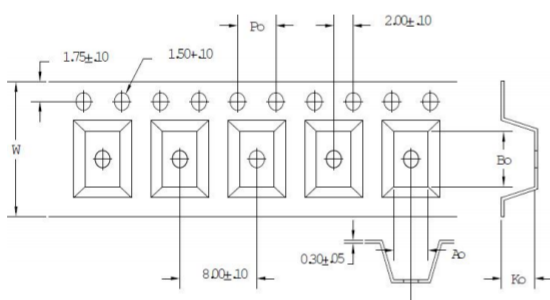
◆ PACKAGING INFORMATION

Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x16mm	178±2	60±2	13.5±0.5	16.7±0.5

Tape Dimension /16 mm

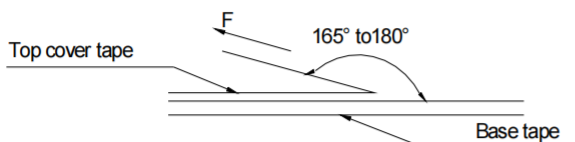


Chip size	W(mm)	Po(mm)	Ao(mm)	Bo(mm)	Ko(mm)
SBH8532S750NTT	16.±0.30	4.0±0.10	3.25±0.10	9.25±0.10	3.05±0.10

Packaging Quantity

Chip size	Chip / Reel	Inner box	Middle box	Carton
SBH8532S750NTT	500	2000	10000	20000

Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300