

## 1. Description

The NVR5124PLT1G(ES) is P-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product NVR5124PLT1G(ES) is Pb-free.

## 2. Features

- -60V,  $R_{DS(ON)}=155m\Omega(Typ.) @V_{GS}=-10V$
- $R_{DS(ON)}=195m\Omega(Typ.) @V_{GS}=-4.5V$
- Use trench MOSFET technology
- High density cell design for low  $R_{DS(on)}$
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

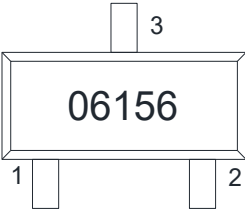
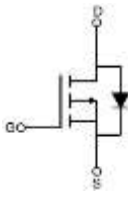
## 3. Applications

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

## 4. Ordering Information

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
NVR5124PLT1G(ES)	SOT-23	06156	Halogen free	Tape & Reel	3,000 PCS	UL 94V-0	7 inches

## 5. Pin Configuration and Functions

Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Source		
3	Drain		

## 6. Specification

### Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$BV_{DSS}$	-60	V
Gate-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current	$I_D$	$T_A=25^{\circ}C$	-1.8
		$T_A=100^{\circ}C$	-1.08
Maximum Power Dissipation	$P_D$	1.2	W
Pulsed Drain Current	$I_{DM}$	-7.2	A
Operating Junction Temperature	$T_J$	-55 to 150	°C
Lead Temperature	$T_L$	260	°C
Storage Temperature Range	$T_{stg}$	-55 to 150	°C

### Thermal resistance ratings

Single Operation				
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance	$R_{\theta JA}$		104	°C/W

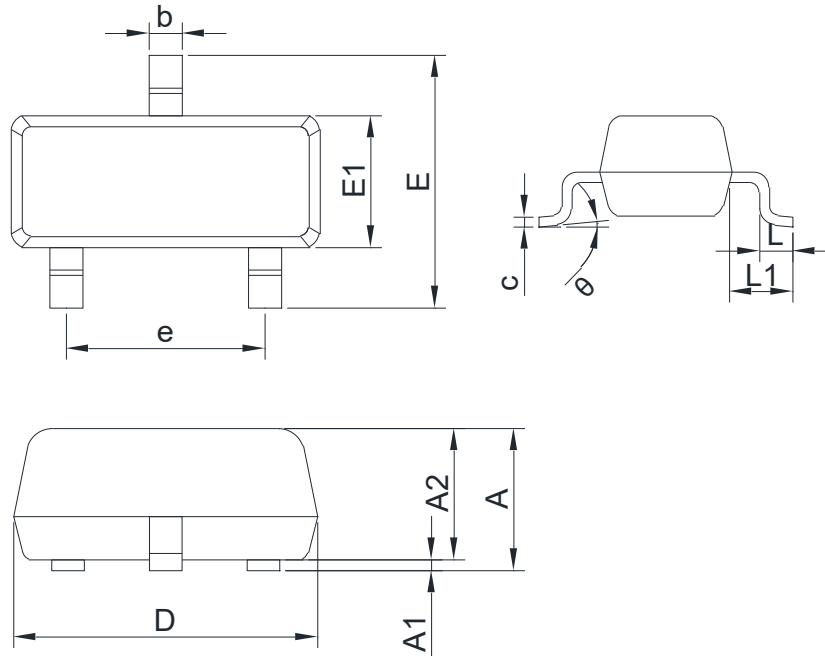
## Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-60			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-60V, V_{GS}=0V$			-1	$\mu A$
Gate-to-source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.1	-1.6	-2.2	V
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-1.5A$		155	201	m $\Omega$
		$V_{GS}=-4.5V, I_D=-1A$		195	253	
<b>CHARGES, CAPACITANCES AND GATE RESISTANCE</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, f=1MHz, V_{DS}=-25V$		533		pF
Output Capacitance	$C_{OSS}$			29		
Reverse Transfer Capacitance	$C_{RSS}$			23		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS}=0 \text{ to } -10V, V_{DS}=-30V, I_D=-1.5A$		9.5		nC
Gate-to-Source Charge	$Q_{GS}$			1.5		
Gate-to-Drain Charge	$Q_{GD}$			2		
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{d(ON)}$	$V_{GS}=-10V, V_{DD}=-30V, I_D=-1.5A, R_G=3\Omega$		45		ns
Rise Time	$t_r$			33		
Turn-Off Delay Time	$t_{d(OFF)}$			15		
Fall Time	$t_f$			11		
<b>BODY DIODE CHARACTERISTICS</b>						
Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=-1.5A$			-1.2	V

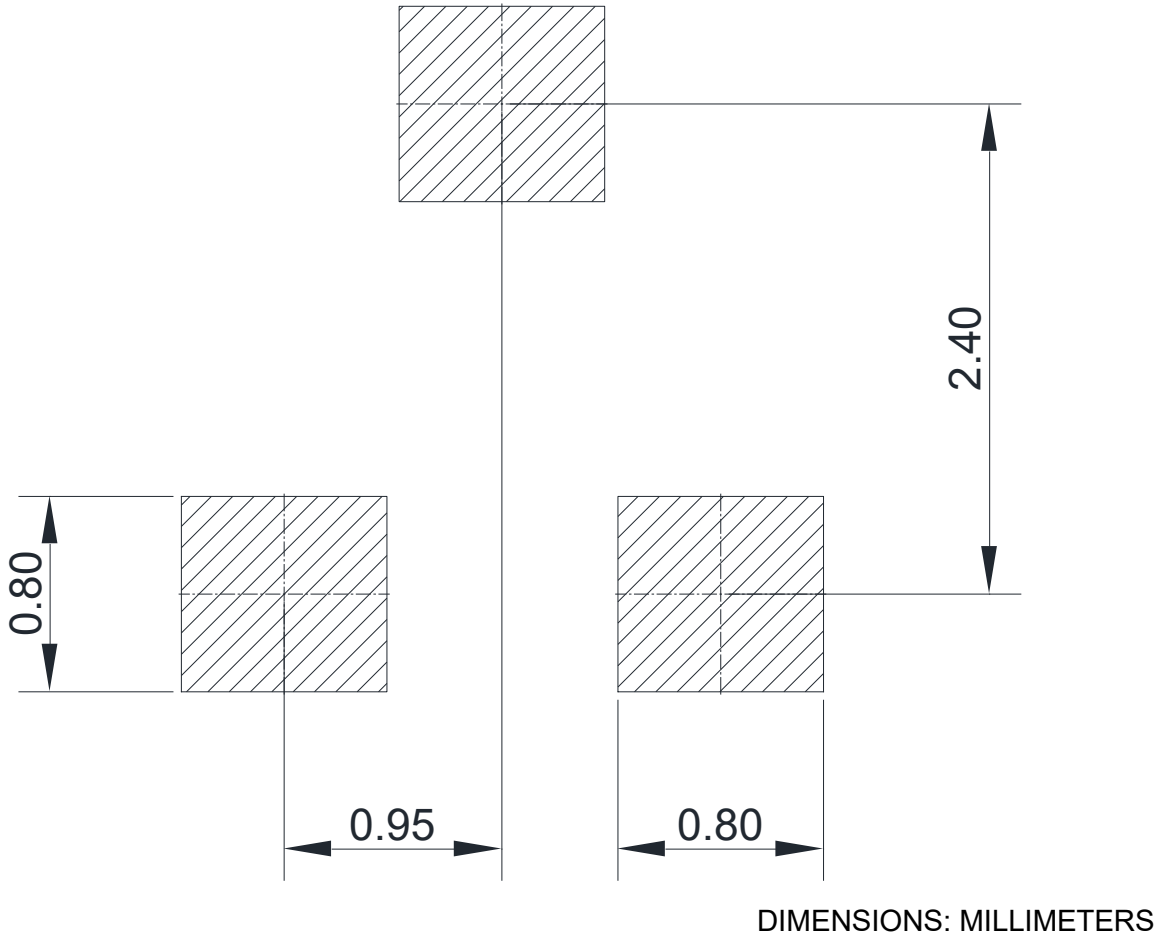
7. Dimension (SOT-23)

POD(Z)



COMMON DIMENSIONS CUNITS MEASURE=MILLIMETER					
SYMBOL	MIN	MAX	SYMBOL	MIN	MAX
A	0.90	1.20	E	2.25	2.55
A1	0.00	0.10	E1	1.20	1.40
A2	0.90	1.10	e	1.80	2.00
b	0.30	0.50	L	0.30	0.50
c	0.07	0.18	L1	0.475	0.625
D	2.80	3.04	$\theta$	0°	8°

8. Recommended Soldering Footprint



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