

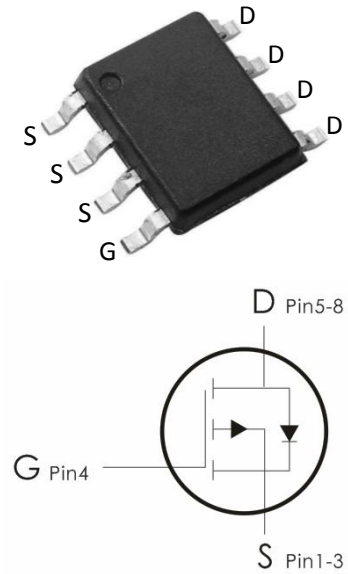
Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=-30V, I_D=-10A, R_{DS(ON)}<15m\Omega @V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.
- 6) MSL3



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DOS4435	S4435	SOP-8	3000 pcs/Reel

Absolute Maximum Ratings: ($T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $T_A=25^\circ C$	-10	A
	Continuous Drain Current- $T_C=100^\circ C$	-7	
I_{DM}	Drain Current-Pulsed ¹	-50	A
E_{AS}	Single pulse avalanche energy ²	47	mJ
P_D	Power Dissipation	4	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ³	30	$^\circ C/W$

Electrical Characteristics: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourctce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	-30	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =-10V	---	---	-1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	-1.1	-1.5	-1.9	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =-10V, I _D =-10A	---	12	15	m Ω
		V _{GS} =-4.5V, I _D =-5A	---	19	23	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1MHz	---	1229	---	pF
C _{oss}	Output Capacitance		---	159	---	
C _{rss}	Reverse Transfer Capacitance		---	145	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} =-15V, V _{GS} =-10V RL=1 Ω ,R _{GEN} =3 Ω	---	17	---	ns
t _r	Rise Time		---	21	---	ns
t _{d(off)}	Turn-Off Delay Time		---	54	---	ns
t _f	Fall Time		---	41	---	ns
Q _g	Total Gate Charge	V _{DS} =-15V , V _{GS} =-10V , I _D =-10A	---	26	---	nC
Q _{gs}	Gate-Source Charge		---	5	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	4	---	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =-10A	---	-0.75	-1	V
I _S	Maximum Continuous Drain to Source Diode Forward Current	V _G =V _D =0V	---	---	-10	A
I _{SM}	Max. Pulsed Forward Currentt	V _G =V _D =0V	---	---	-50	A
T _{rr}	ReverseRecoveryTime	V _{GS} =0V, I _S =-10A	---	32	---	ns
Q _{rr}	ReverseRecoveryCharge	di/dt=100A/μs	---	28	---	nc

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting $T_J=25^{\circ}\text{C}$, $V_{DD}=30\text{V}$, $V_G=10\text{V}$, $R_G=25\text{ohm}$, $L=0.5\text{mH}$, $I_{AS}=13.5\text{A}$, $V_{DD}=0\text{V}$ during time in avalanche.
 3. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB.
 4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Characteristics: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

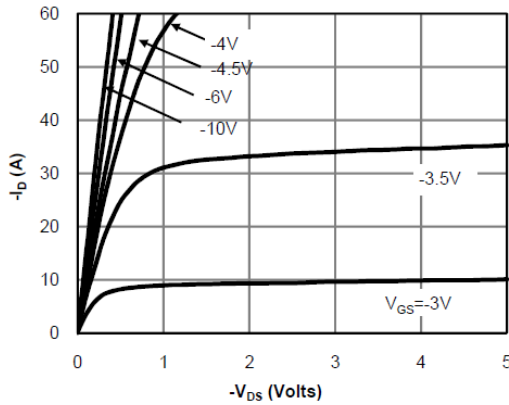


Figure 1. On-Regin Characteristics

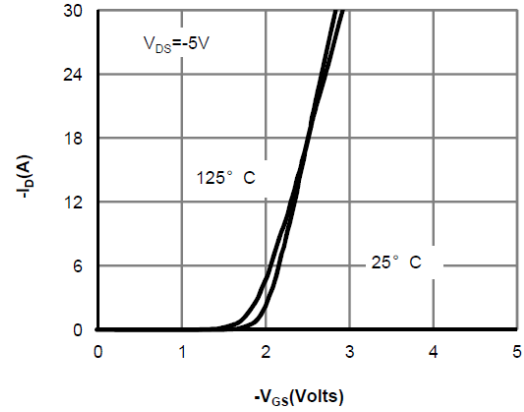


Figure 2. Transfer Characteristics

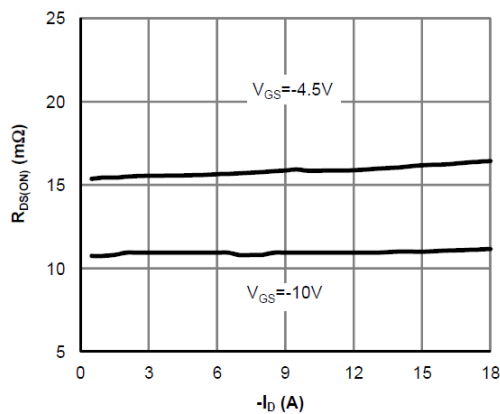


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

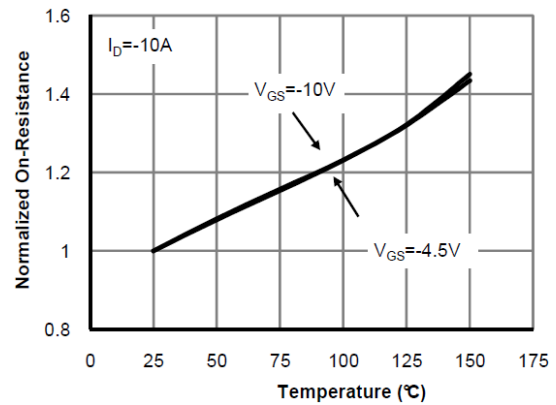


Figure 4. On-Resistance vs. Junction Temperature

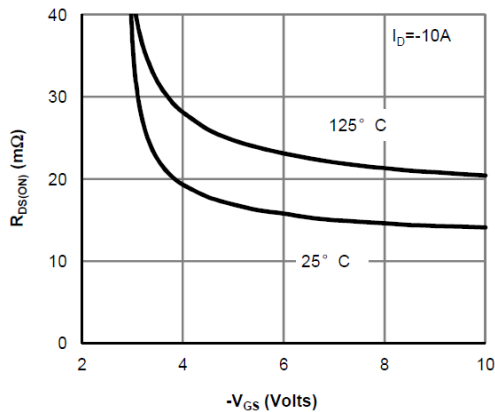


Figure 5. On-Resistance vs. Gate-Source Voltage

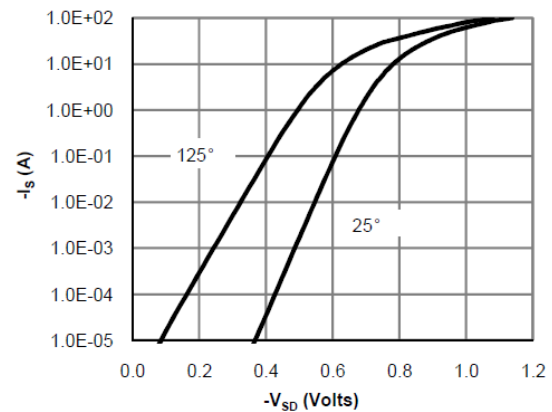


Figure 6 Body Diode Characteristics

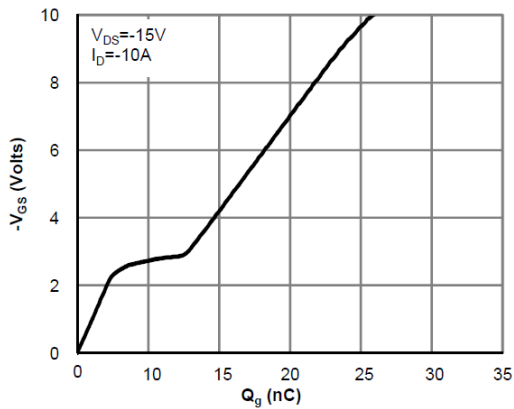


Figure 7. Gate-Charge Characteristics

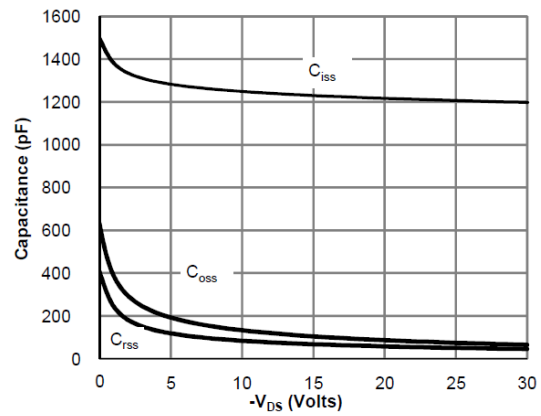


Figure 8. Capacitance Characteristics

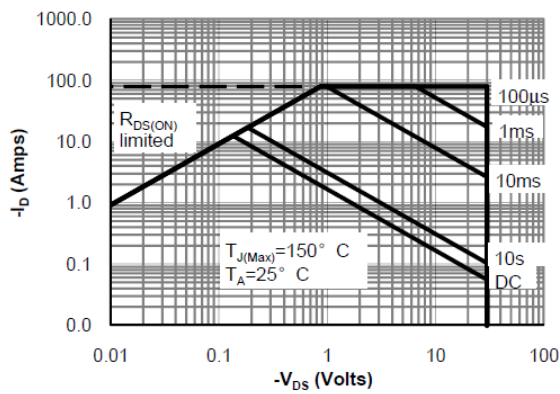


Figure 9. Maximum Forward Biased Safe Operating Area

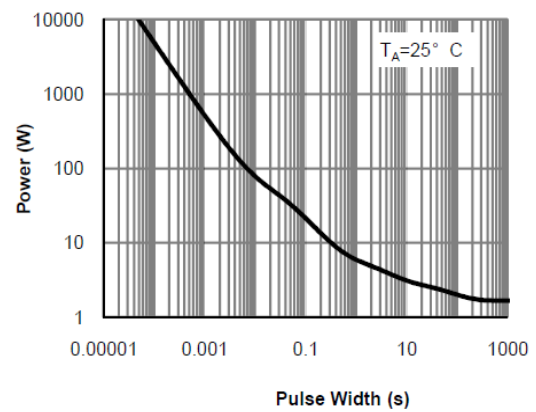


Figure 10. Single Pulse Power Rating Junction to Ambient

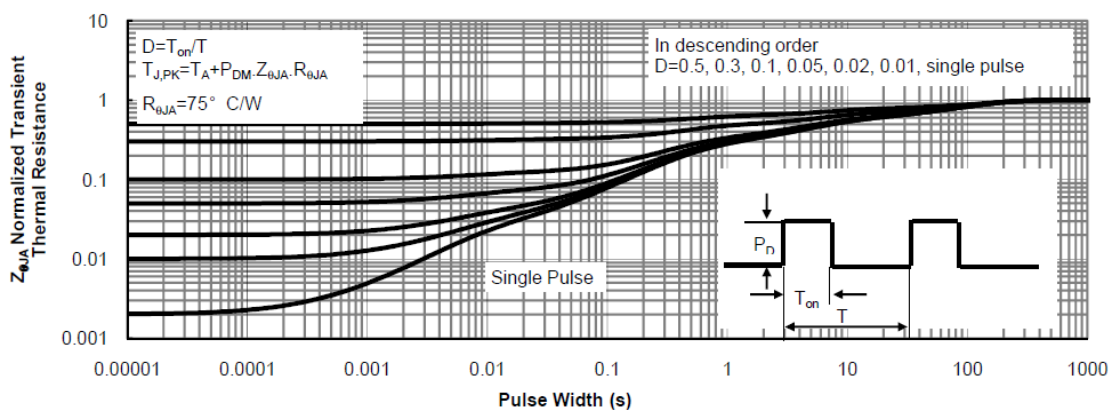
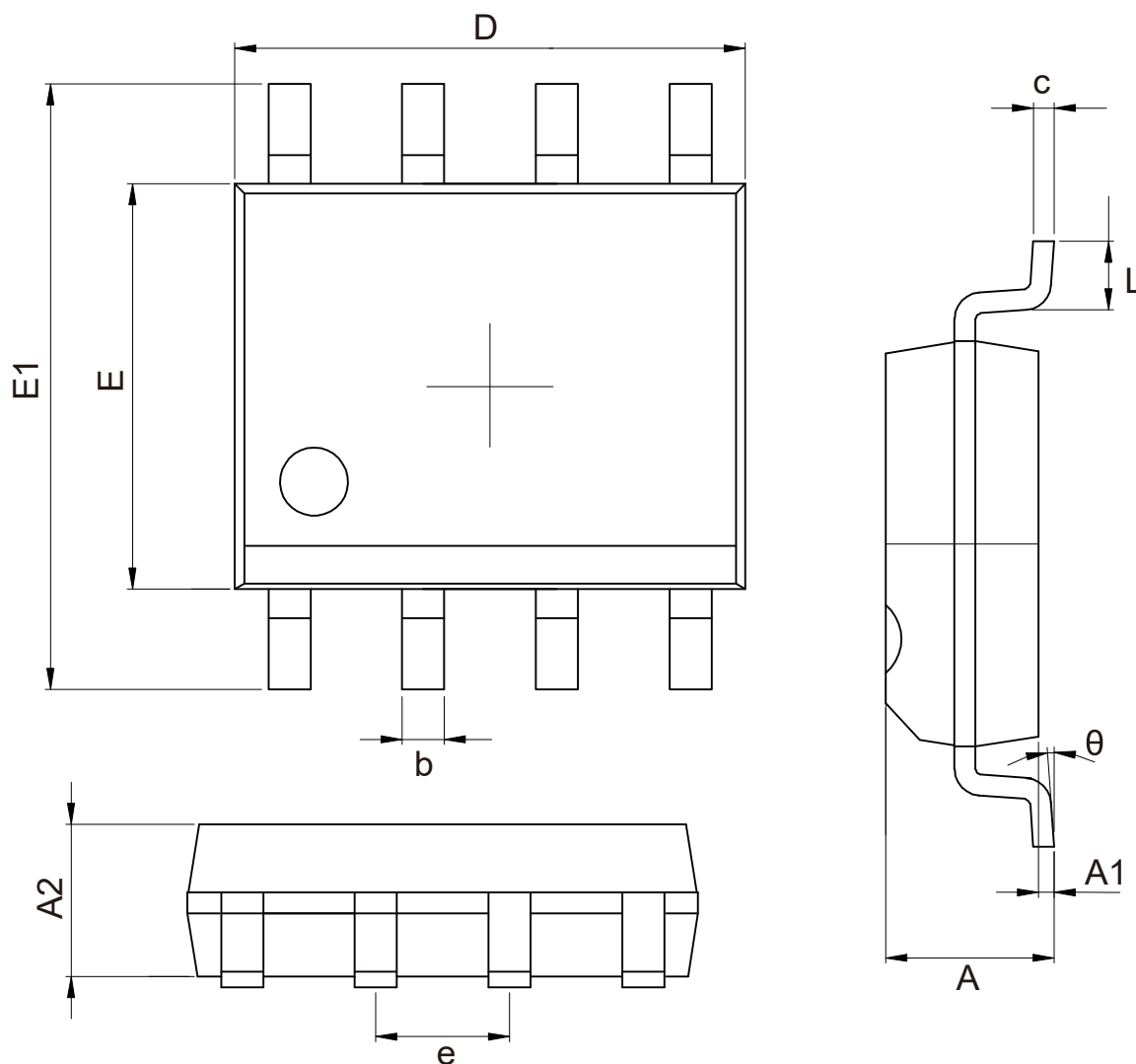


Figure 11. Normalized Maximum Transient Thermal Impedance

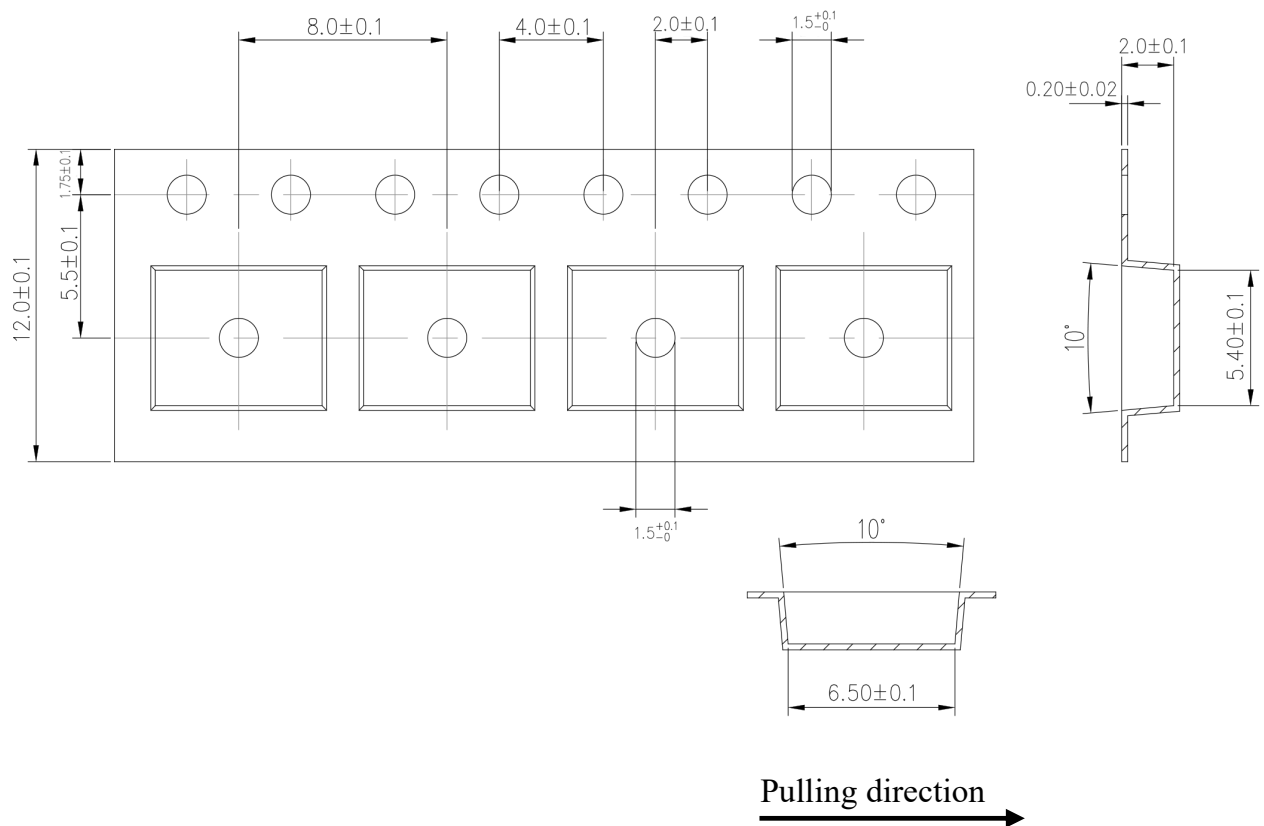
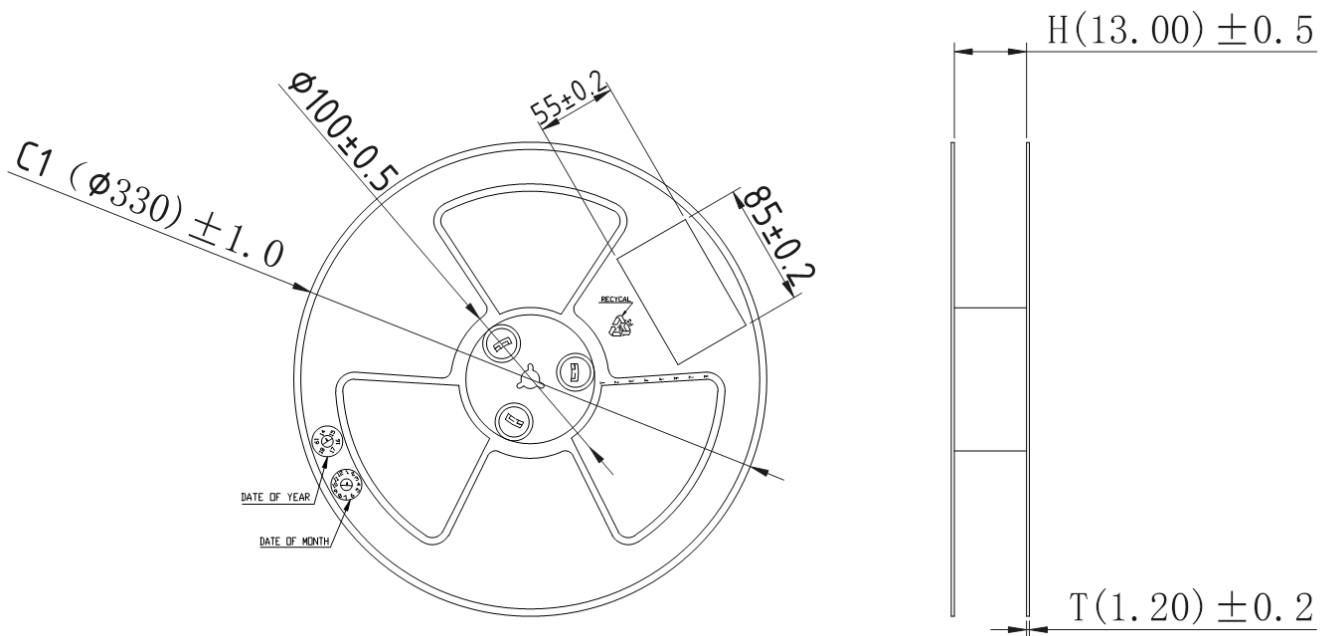
SOP-8 Package Information:


COMMON DIMENSIONS			
CUNITS MEASURE=MILLIMETER			
SYMBOL	MIN	NOM	MAX
A	1.350	—	1.750
A1	0.100	—	0.250
A2	1.350	—	1.550
b	0.330	—	0.510
c	0.170	—	0.250
D	4.700	—	5.100
E	3.800	3.900	4.000
E1	5.800	—	6.200
e	1.270BSC		
L	0.400	—	1.270
θ	0°	--	8°

Unit:mm

Tape & Reel Information

Dimensions in mm



Marking Information:

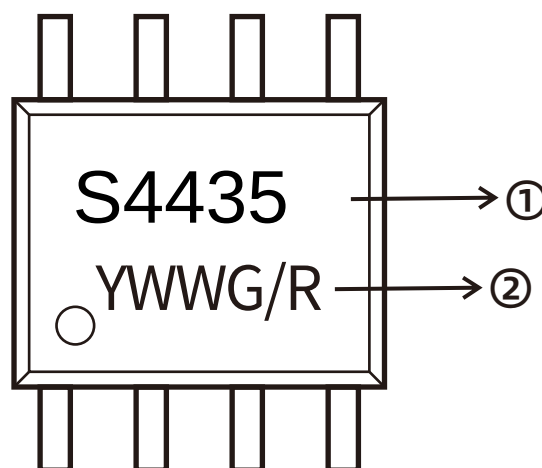
①. Part NO.

②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.1	2024-03-21	Release of final version

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