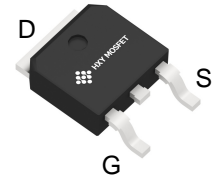




## Description

The DMP4015SK3Q-13 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

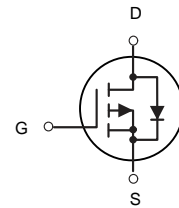


**TO-252-2L**

## General Features

$V_{DS} = -40V$   $I_D = -80A$

$R_{DS(ON)} < 8.2\ m\Omega @ V_{GS}=10V$



P-Channel MOSFET

## Application

Battery protection

Load switch

Uninterruptible power supply

## Ordering Information

Product ID	Pack	Brand	Qty(PCS)
DMP4015SK3Q-13	TO-252-2L	HXY MOSFET	2500

## Absolute Maximum Ratings ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-40	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	-80	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	-50	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	-320	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation <sup>4</sup>	81	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	54	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	1.54	°C/W



**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-40	-	-	V
Gate-body Leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	V <sub>DS</sub> = -40V, V <sub>GS</sub> = 0V	-	-	-1	pA
	T <sub>J</sub> =100°C		-	-	-100	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-1.0	-1.6	-2.5	V
Drain-Source on-Resistance <sup>4</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -20A	-	6.4	8.2	mΩ
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -15A	-	8.2	11	
Forward Transconductance <sup>4</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -20A	-	104	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V, f = 1MHz	-	5295	-	pF
Output Capacitance	C <sub>oss</sub>		-	430	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	385	-	
Gate Resistance	R <sub>g</sub>	f = 1MHz	-	4.3	-	Ω
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -20V, I <sub>D</sub> = -20A	-	110	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	12.5	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	23	-	
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = -10V, V <sub>DD</sub> = -20V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = -20A	-	16.8	-	ns
Rise Time	t <sub>r</sub>		-	10	-	
Turn-off Delay Time	t <sub>d(off)</sub>		-	65	-	
Fall Time	t <sub>f</sub>		-	17	-	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -20A, dI/dt = 100A/μs	-	42	-	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	29	-	nC
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	I <sub>S</sub> = -20A, V <sub>GS</sub> = 0V	-	-	-1.2	V
Continuous Source Current	T <sub>C</sub> =25°C	I <sub>S</sub>	-	-	-80	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>= -30V, V<sub>GS</sub>= -10V, L= 0.1mH, I<sub>AS</sub>= -45A.
3. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.



### Typical Characteristics

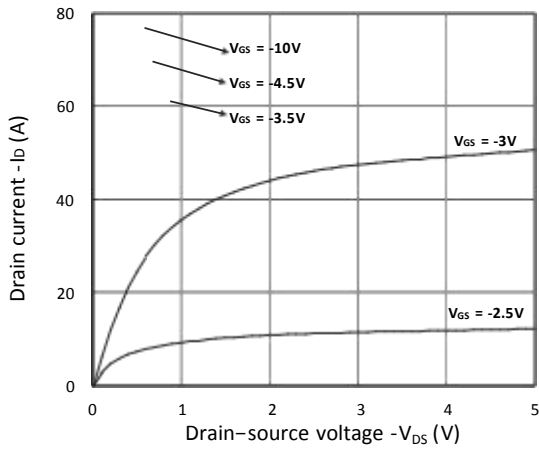


Figure 1. Output Characteristics

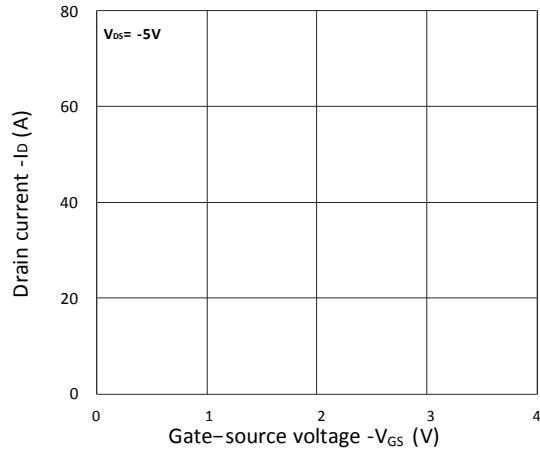


Figure 2. Transfer Characteristics

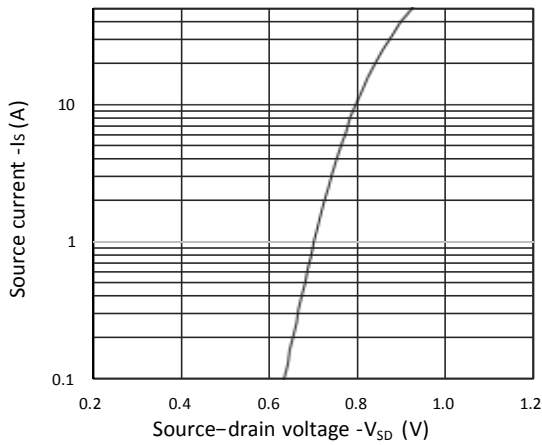


Figure 3. Forward Characteristics of Reverse

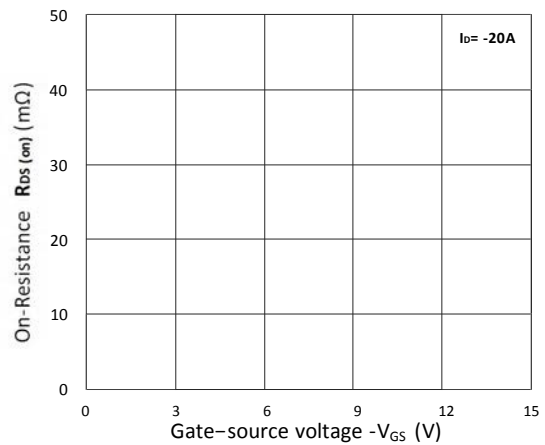


Figure 4.  $R_{DS(ON)}$  vs  $V_{GS}$

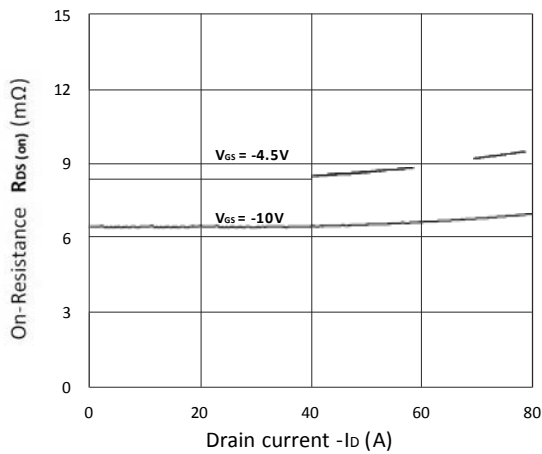


Figure 5.  $R_{DS(ON)}$  vs  $I_D$

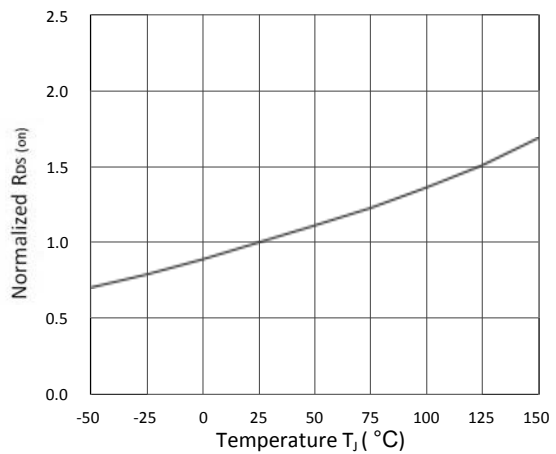


Figure 6. Normalized  $R_{DS(ON)}$  vs Temperature

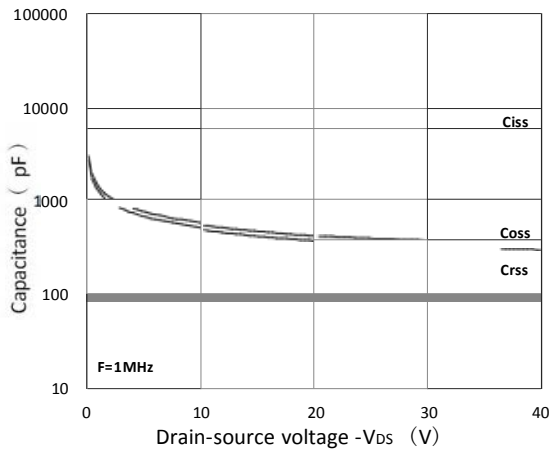


Figure 7. Capacitance Characteristics

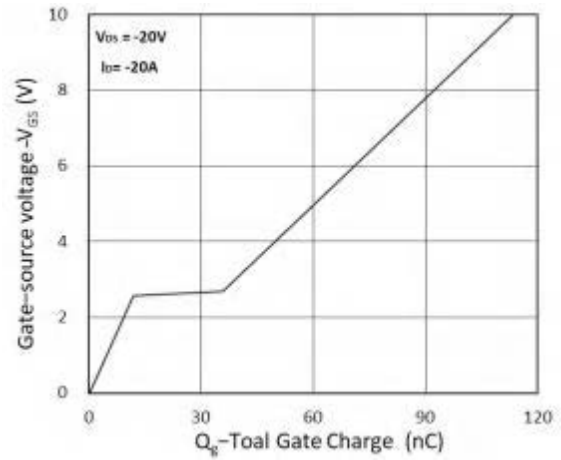


Figure 8. Gate Charge Characteristics

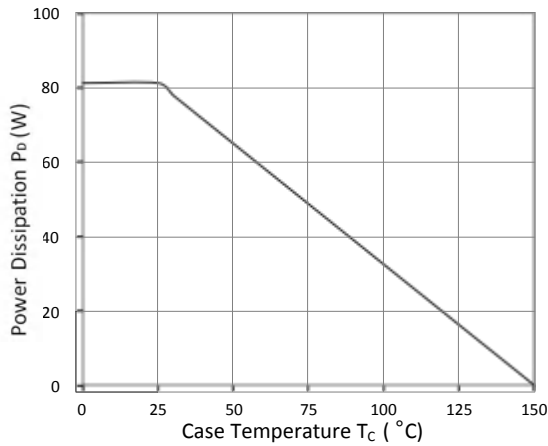


Figure 9. Power Dissipation

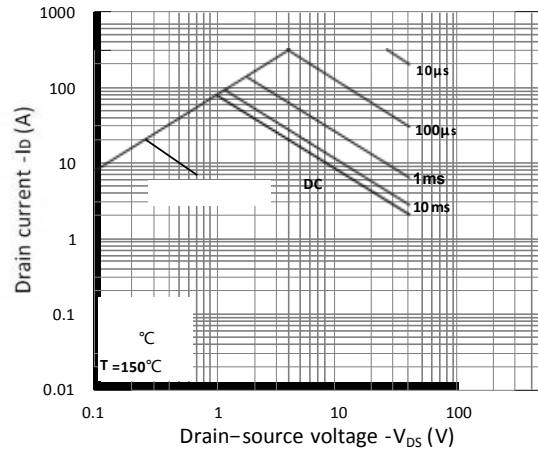


Figure 10. Safe Operating Area

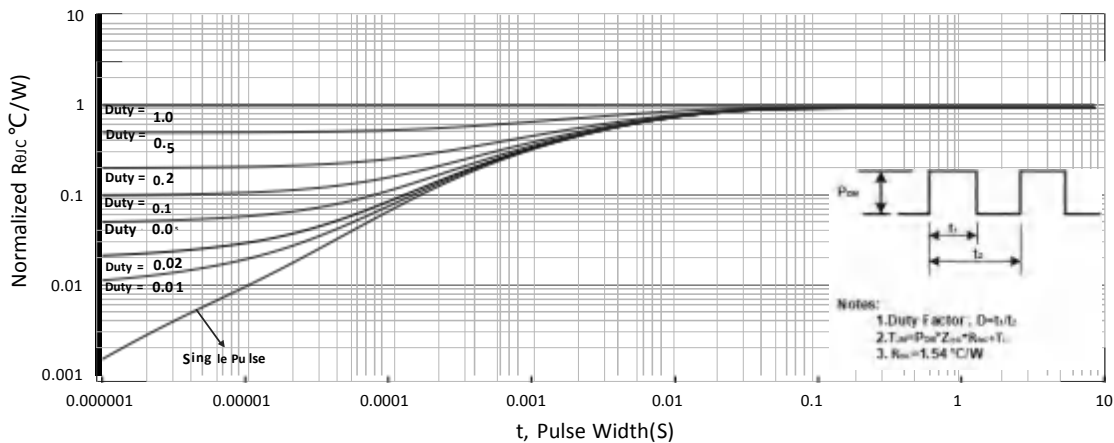


Figure 11. Normalized Maximum Transient Thermal Impedance



### Test Circuit

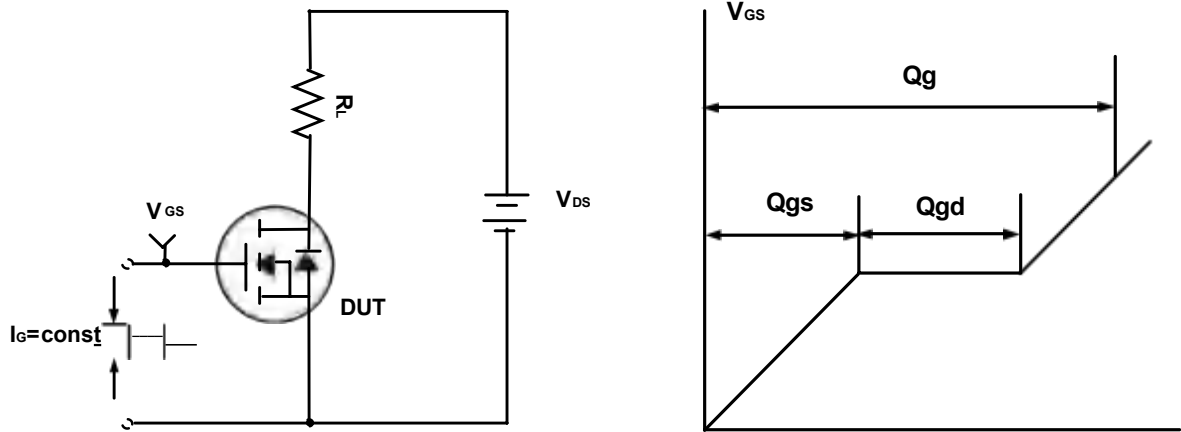


Figure A. Gate Charge Test Circuit & Waveforms

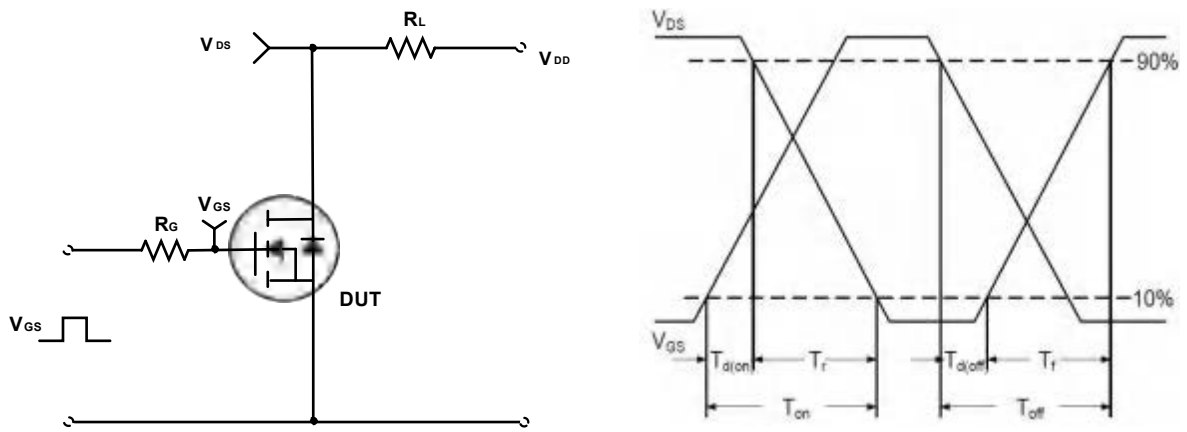


Figure B. Switching Test Circuit & Waveforms

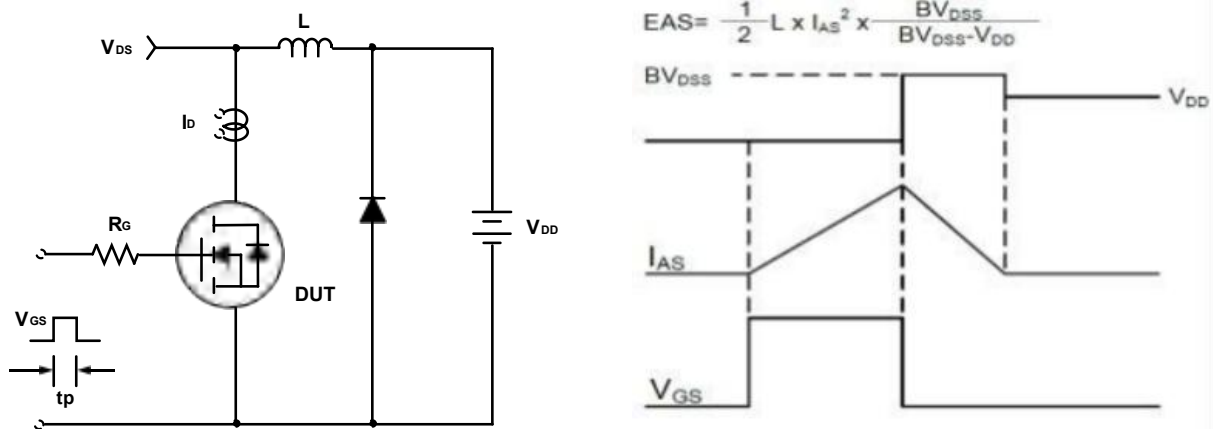
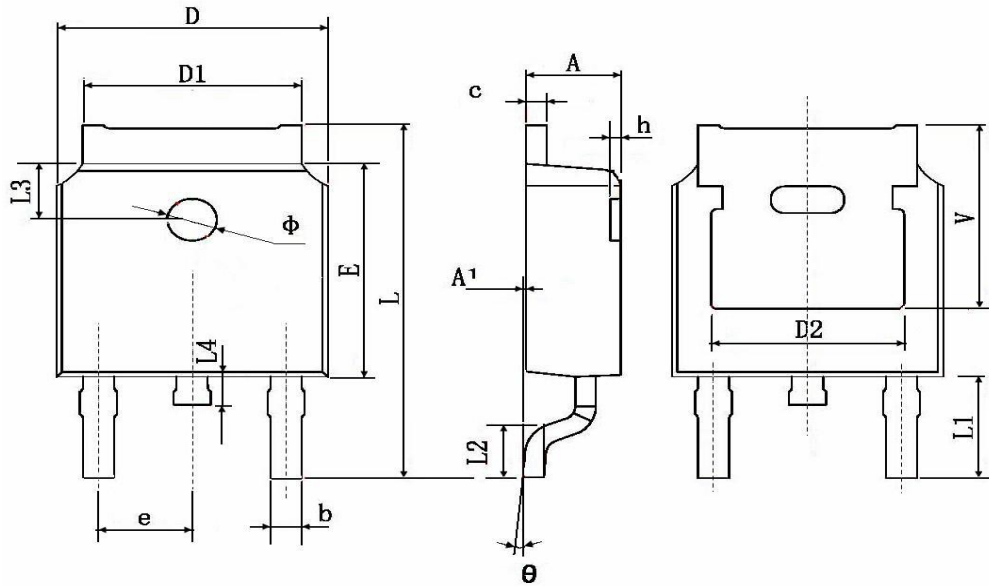


Figure C. Unclamped Inductive Switching Circuit & Waveforms



### TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	



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