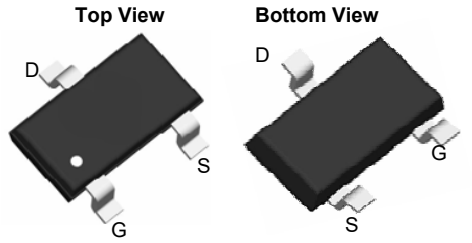
Features

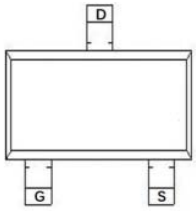
- -18V, -7A
  $R_{DS(ON)}$ ***TYP*** = 19mΩ @ V_{GS} = -4.5V
  $R_{DS(ON)}$ ***TYP*** = 25mΩ @ V_{GS} = -2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free



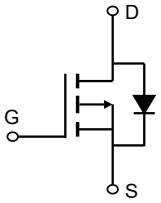
SOT-23-3L

Applications

- Load Switch
- PWM Application
- Power Management



Pin Assignment



Schematic Diagram

Absolute Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		-18	V
V_{GS}	Gate-to-Source Voltage		±12	V
I_D	Continuous Drain Current	T_A = 25°C	-7	A
		T_A = 100°C	-4.1	
I_{DM}	Pulsed Drain Current [1]		-28	A
P_D	Power Dissipation	T_A = 25°C	1.4	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient[2]		89	°C/W
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	℃

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = -250μA, V_{GS} = 0V	-15	-18	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = -15V, V_{GS} = 0V	-	-	-1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±12V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = -250μA	-0.4	-0.6	-1.0	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(3)	V_{GS} = -4.5V, I_D =-5A	-	19	22	mΩ
		V_{GS} = -2.5V, I_D = -3A	-	25	31	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = -10V, f = 1MHz	-	860	-	pF
C_{oss}	Output Capacitance		-	162	-	pF
C_{rss}	Reverse Transfer Capacitance		-	150	-	pF
Q_g	Total Gate Charge	V_{GS} = -4.5V, V_{DS} = -10V, I_D =-3A	-	8	-	nC
Q_{gs}	Gate Source Charge		-	1.2	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	1.6	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = -4.5V, V_{DD} = -6V, I_D= -3A, R_{GEN} = 3Ω	-	13	-	ns
t_r	Turn-On Rise Time		-	35	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	32	-	ns
t_f	Turn-Off Fall Time		-	10	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-7	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-28	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S = -7A	-	-	-1.2	V
trr	Body Diode Reverse Recovery Time	I_F = -3A, di/dt = 80A/us	-	20	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	9	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Test Circuit

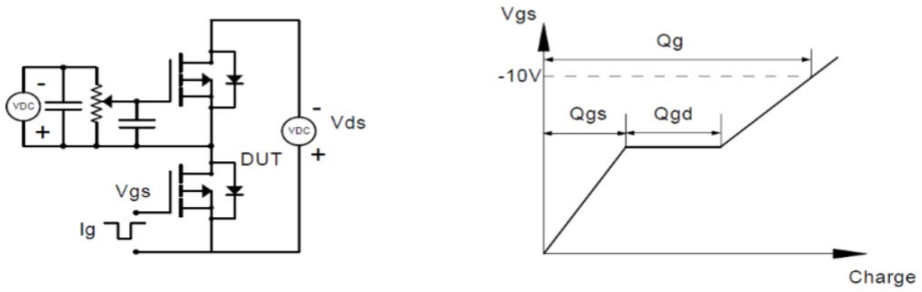


Figure 1: Gate Charge Test Circuit & Waveform

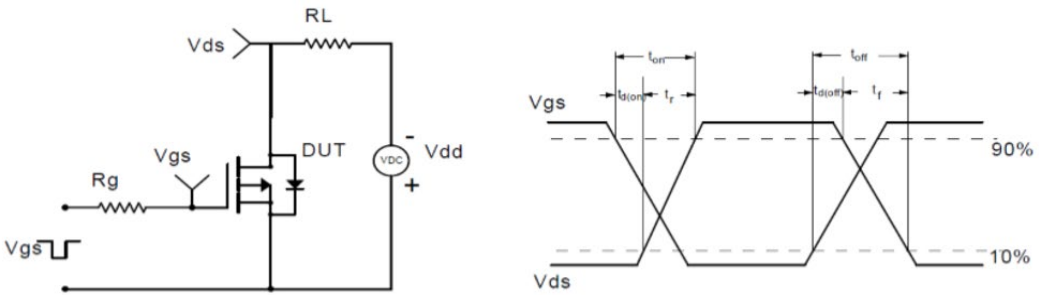


Figure 2: Resistive Switching Test Circuit & Waveform

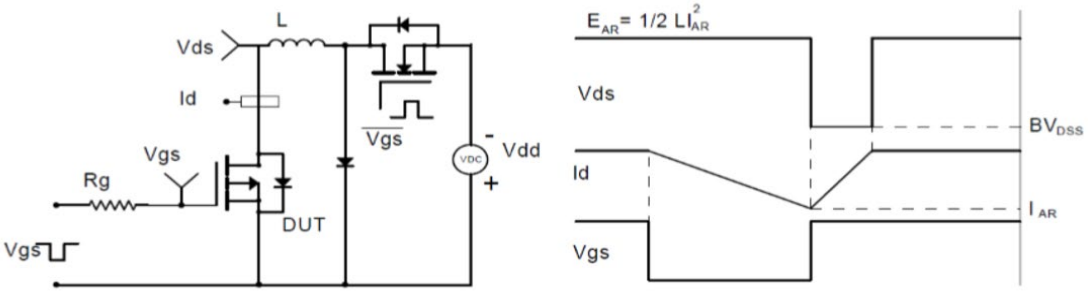


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

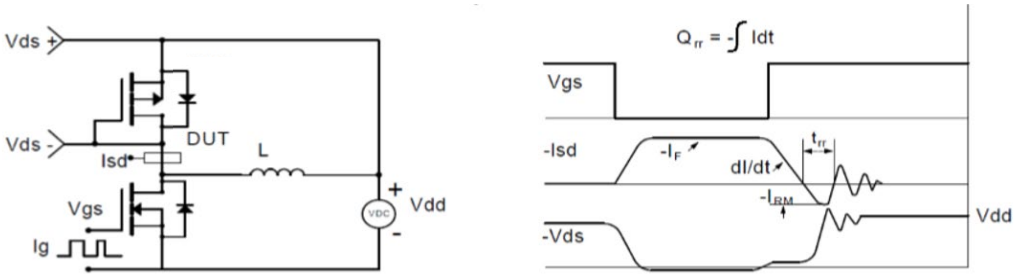
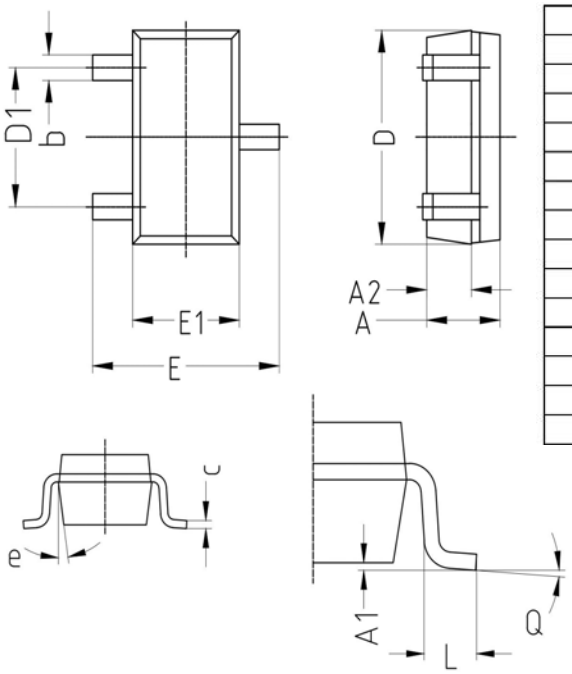


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOT-23-3L)



COMMON DIMENSION (MM)			
PKG	SOT-23-3L		
Symbol	MIN	MON	MAX
A	1.080	1.100	1.120
A1	0.010	0.060	0.150
A2	0.640	0.670	0.700
b	0.325	0.350	0.375
c	0.125	0.135	0.150
D	2.92	2.930	2.980
D1	1.875	1.900	1.925
E	2.650	2.800	2.950
E1	1.580	1.600	1.670
L	0.300	0.450	0.600
e		8°	
Q	0°	4°	8°