

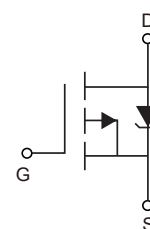
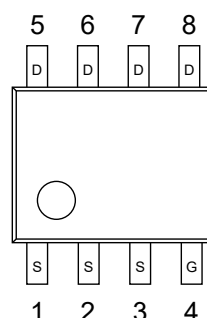
1.Features

- $V_{DS(V)} = -40V$
- $I_D = -6.2A$
- $R_{DS(ON)} < 41m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 70m\Omega (V_{GS} = -4.5V)$
- Trench Technology
- Ultra Low On-Resistance
- P-Channel MOSFET
- Lead-Free

2.Pinning information

Pin	Symbol	Description
1,2,3	S	SOURCE
4	G	GATE
5,6,7,8	D	DRAIN

SOP-8



3.Absolute Maximum Ratings $T_A = 25^\circ C$ unless otherwise noted

Parameter		Symbol	Rating	Units
Drain- Source Voltage		V_{DS}	-40	V
Continuous Drain Current, $V_{GS} = -10V$	$T_A = 25^\circ C$	I_D	-6.2	A
Continuous Drain Current, $V_{GS} = -10V$	$T_A = 70^\circ C$		-4.9	A
Pulsed Drain Current ①		I_{DM}	-25	A
Power Dissipation ③	$T_A = 25^\circ C$	P_D	2.5	W
Power Dissipation ③	$T_A = 70^\circ C$		1.6	W
Linear Derating Factor			20	mW/ $^\circ C$
Gate-to-Source Voltage		V_{GS}	± 20	V
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$



4. Thermal resistance rating

Parameter	Symbol	Typ	Max	Units
Junction-to-Drain Lead	$R_{\theta JL}$		20	$^{\circ}\text{C/W}$
Junction-to-Ambient ③	$R_{\theta JA}$		50	$^{\circ}\text{C/W}$

5. Electrical Characteristics $T_J=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=-250\mu\text{A}$, $V_{GS}=0\text{V}$	-40			V
Breakdown Voltage Temp. Coefficient	$\Delta V_{(BR)DSS}/T_J$	$I_D=-1\text{mA}$, Reference to 25°C		0.03		$\text{V}/^{\circ}\text{C}$
Static Drain-to-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10\text{V}$, $I_D=-6.2\text{A}$ ②		25	41	m Ω
		$V_{GS}=-4.5\text{V}$, $I_D=-5\text{A}$ ②		45	70	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	-1		-3	V
Forward Transconductance	g_{FS}	$V_{DS}=-10\text{V}$, $I_D=-6.2\text{A}$	8.9			S
Drain-to-Source Leakage Current	I_{DSS}	$V_{DS}=-32\text{V}$, $V_{GS}=0\text{V}$			-10	μA
		$V_{DS}=-32\text{V}$, $V_{GS}=0\text{V}$, $T_J=70^{\circ}\text{C}$			-25	
Gate-to-Source Forward Leakage	I_{GSS}	$V_{GS}=-20\text{V}$			-100	nA
Gate-to-Source Reverse Leakage		$V_{GS}=20\text{V}$			100	
Total Gate Charge	Q_g	$I_D=-6.2\text{A}$		53	80	nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=-32\text{V}$		14	21	
Gate-to-Drain ("Miller") Charge	Q_{gd}	$V_{GS}=-10\text{V}$		3.9	5.9	
Turn-On Delay Time	$t_{D(on)}$	$V_{DD}=-20\text{V}$ ②		24		ns
Rise Time	t_r	$I_D=-1\text{A}$		280		
Turn-Off Delay Time	$t_{D(off)}$	$R_G=6\Omega$		210		
Fall Time	t_f	$V_{GS}=-10\text{V}$		100		
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}$		3220		pF
Output Capacitance	C_{oss}	$V_{DS}=-25\text{V}$		160		
Reverse Transfer Capacitance	C_{rss}	$f=1.0\text{kHz}$		190		



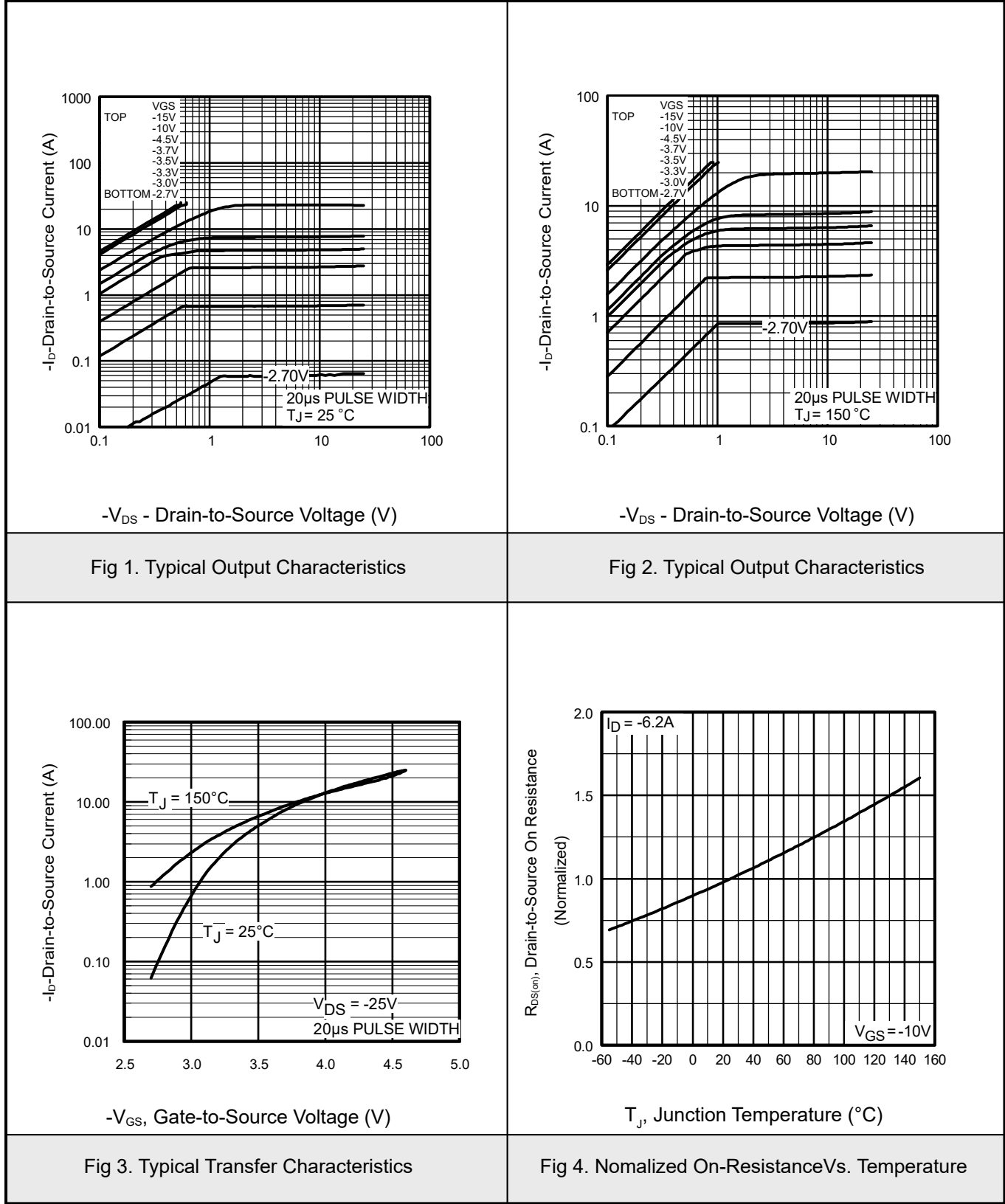
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_S	MOSFET symbol showing the integral reverse p-n junction diode.			-2.5	A
Pulsed Source Current (Body Diode) ①	I_{SM}				-25	
Diode Forward Voltage	V_{SD}	$T_J=25^{\circ}\text{C}, I_S=-2.5\text{A}, V_{GS}=0\text{V}$ ②			-1.2	V
Reverse Recovery Time	t_{rr}			32	48	ns
Reverse Recovery Charge	Q_{rr}			45	68	nC

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- ③ Surface mounted on 1 in square Cu board.



6.1 Typical Characteristics





6.2 Typical Characteristics

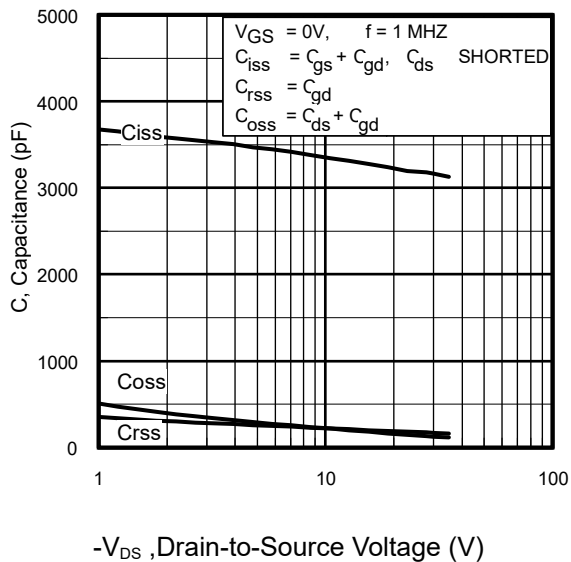


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

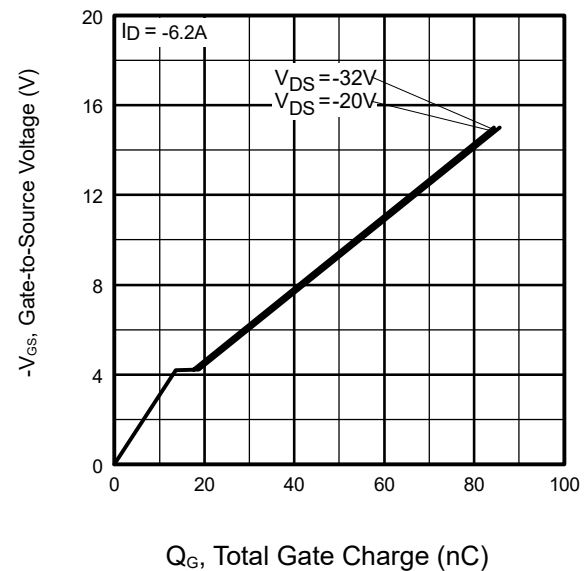


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

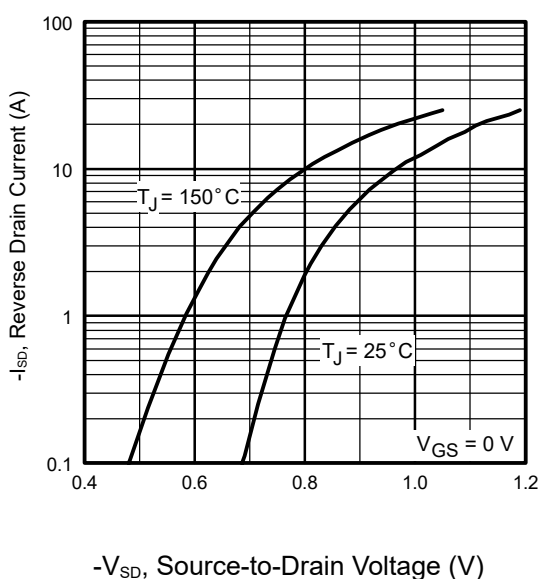


Fig 7. Typical Source-Drain Diode Forward Voltage

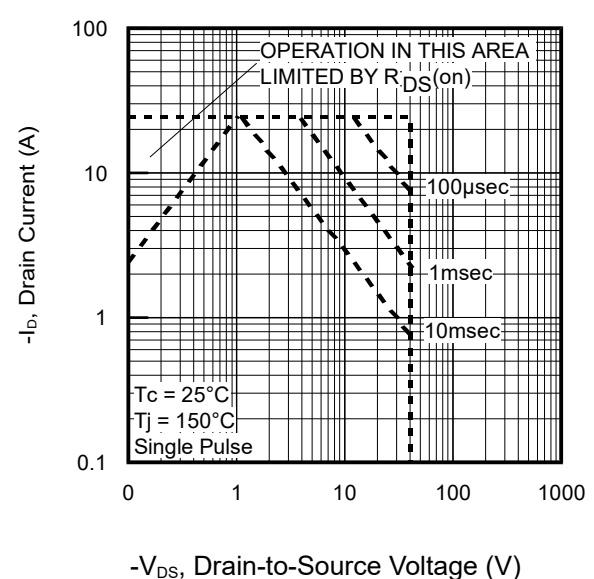
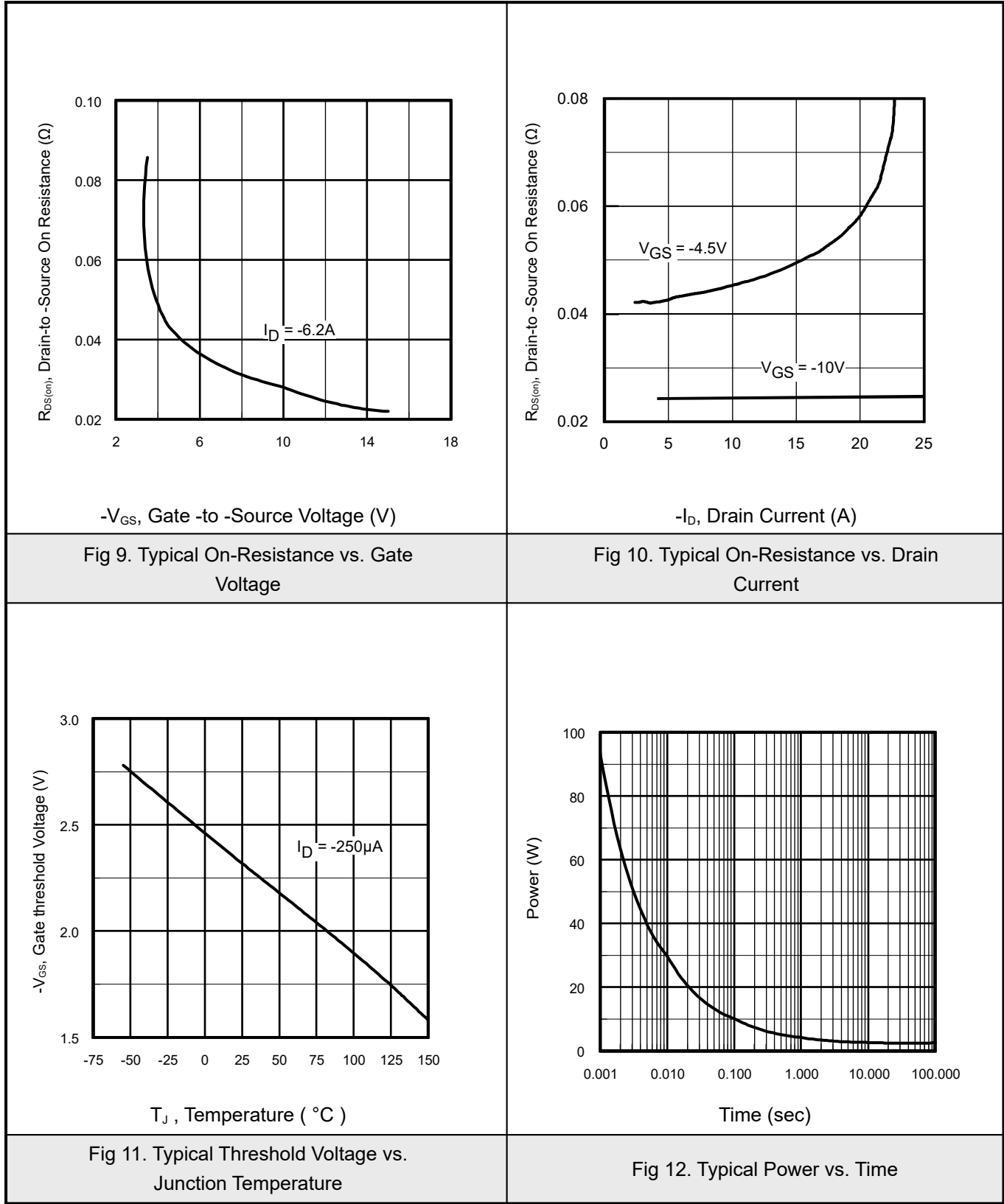


Fig 8. Maximum Safe Operating Area



6.3 Typical Characteristics





6.4 Typical Characteristics

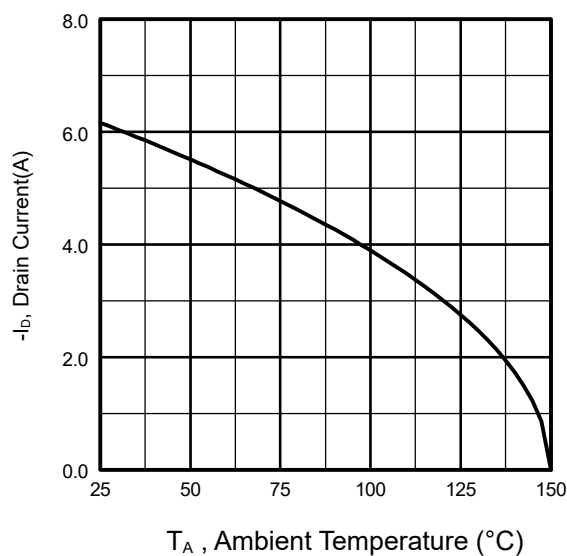


Fig 13. Maximum Drain Current vs. Ambient Temperature

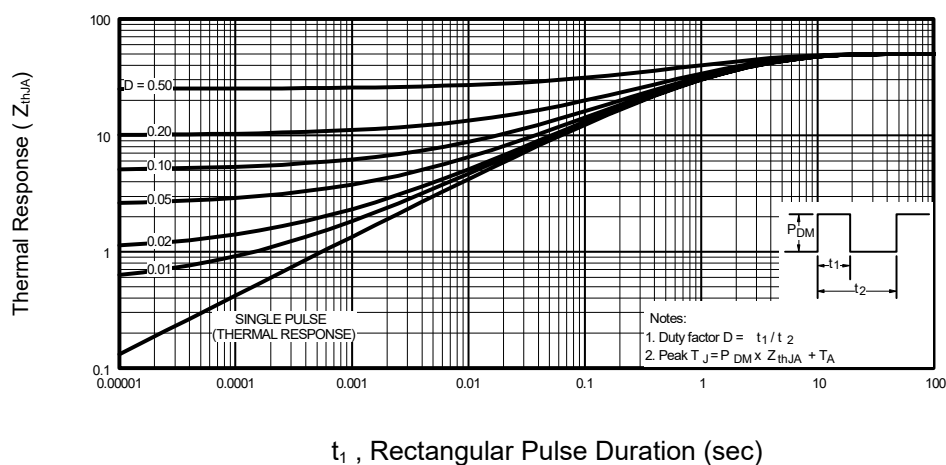


Fig 14. Typical Effective Transient Thermal Impedance, Junction-to-Ambient

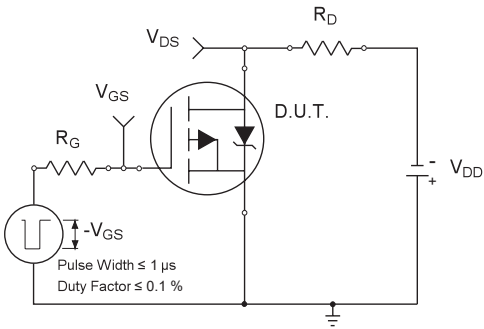


Fig 15a. Switching Time Test Circuit

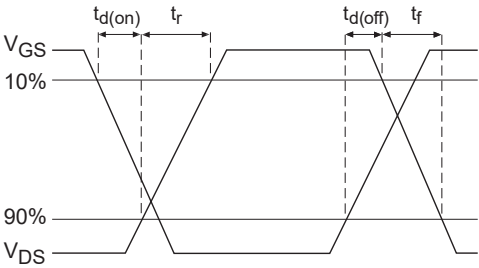


Fig 15b. Switching Time Waveforms

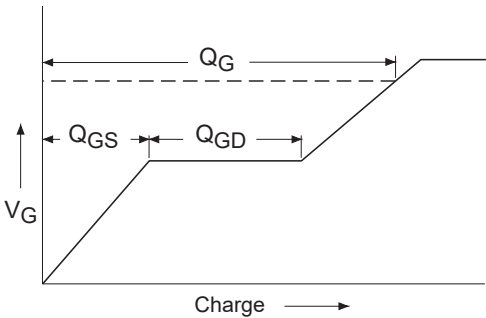


Fig 16a. Basic Gate Charge Waveform

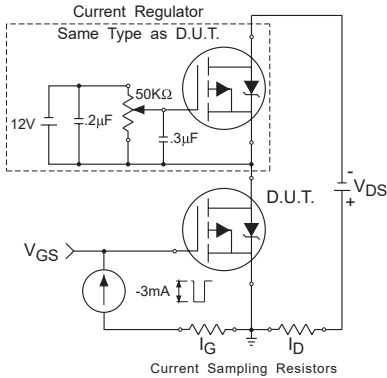
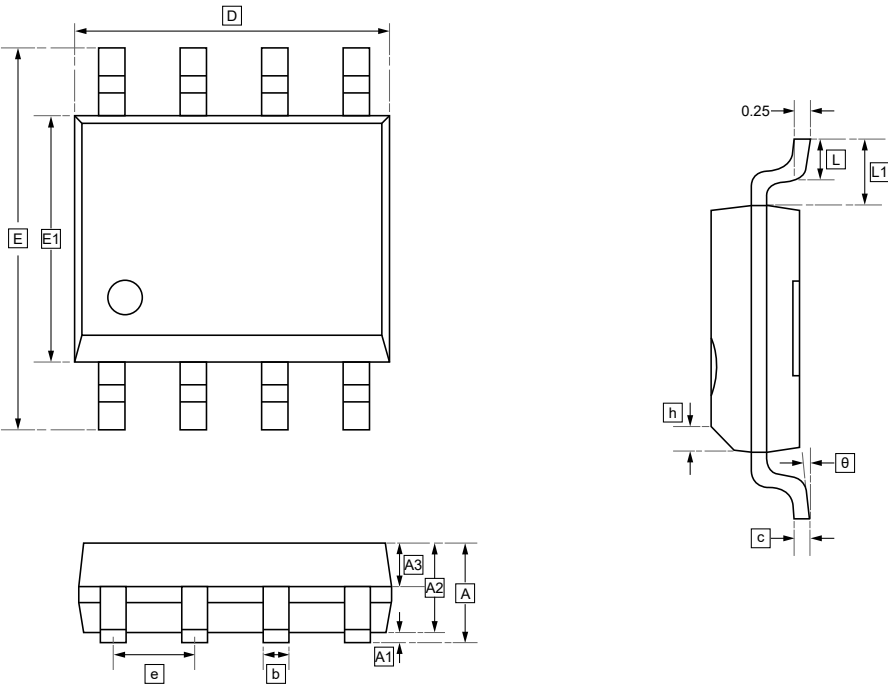


Fig 16b. Gate Charge Test Circuit



7.SOP-8 Package Outline Dimensions



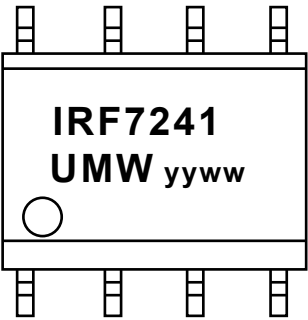
DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	A3	b	c	D	E	E1	e	h	L
Min	-	0.05	1.30	0.60	0.39	0.20	4.80	5.80	3.80	1.24	0.30	0.50
Max	1.75	0.20	1.50	0.70	0.47	0.24	5.00	6.20	4.00	1.30	0.50	0.80

Symbol	L1	θ
Min	1.00	0°
Max	1.10	8°



8.Ordering information



yy: Year Code
ww: Week Code

Order Code	Package	Base QTY	Delivery Mode
UMW IRF7241TR	SOP-8	3000	Tape and reel



9.Disclaimer

UMW reserves the right to make changes to all products, specifications. Customers should obtain the latest version of product documentation and verify the completeness and currency of the information before placing an order.

When applying our products, please do not exceed the maximum rated values, as this may affect the reliability of the entire system. Under certain conditions, any semiconductor product may experience faults or failures. Buyers are responsible for adhering to safety standards and implementing safety measures during system design, prototyping, and manufacturing when using our products to prevent potential failure risks that could lead to personal injury or property damage.

Unless explicitly stated in writing, UMW products are not intended for use in medical, life-saving, or life-sustaining applications, nor for any other applications where product failure could result in personal injury or death. If customers use or sell the product for such applications without explicit authorization, they assume all associated risks.

When reselling, applying, or exporting, please comply with export control laws and regulations of China, the United States, the United Kingdom, the European Union, and other relevant countries, regions, and international organizations.

This document and any actions by UMW do not grant any intellectual property rights, whether express or implied, by estoppel or otherwise. The product names and marks mentioned herein may be trademarks of their respective owners.