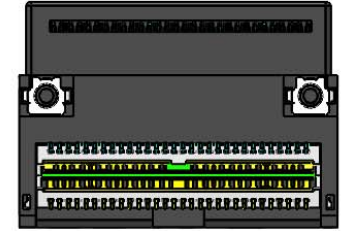


UE62-A1020-X000X  
(WITHOUT SOLDER RING  
FOR SINGLE SIDE MOUNTING )



UE62-A1020-X100X  
(WITH SOLDER RING FOR  
BELLY TO BELLY MOUNTING )

REVISIONS				
REV.	ECN	DESCRIPTION	DATE	APPROVED
C		ADD REVERSED PACKING OPTION	JUN.12.20	J.SI
D		UPDATED DRAWING	OCT.14.20	J.SI
E		ADD PLUG AS COVER	MAR.01.21	J.SI
F		REMOVED COVER	OCT.21.22	J.SI
G		ADD THE HOUSING UL NUMBER	SEP.04.23	J.SI

**AMPHENOL PART NUMBER CONFIGURATION**

UE62-A1020-XX00X

- PACKAGING
    - T = T&R PACKAGING (STANDARD CONNECTOR ORIENTATION)
    - R = T&R PACKAGING (REVERSED CONNECTOR ORIENTATION)
  - SOLDER RING CONFIGURATION
    - 0 = WITHOUT SOLDER RING
    - 1 = WITH SOLDER RING
  - PLATING OPTION
    - 2 = 0.76um GOLD AT MATING AREA  
GOLD FLASH IN TERMINATION
    - 3 = 0.76um GOLD AT MATING AREA  
2.54-7.62um MATTE TIN IN TERMINATION
    - 5 = 0.38um GOLD AT MATING AREA  
GOLD FLASH IN TERMINATION
    - 6 = 0.38um GOLD AT MATING AREA  
2.54-7.62um MATTE TIN IN TERMINATION
- DO NOT SCALE DRAWING**

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NOTES:

1) MATERIAL:

- HOUSING: SOLDER REFLOW PROCESS COMPATIBLE LCP, UL 94 V-0,(UL NUMBER IS E83005)
- CONTACTS: COPPER ALLOY. PLATING: SEE PART NUMBER CONFIGURATION.
- SOLDER RINGS: COPPER ALLOY. PLATING: MATTE TIN PLATED.

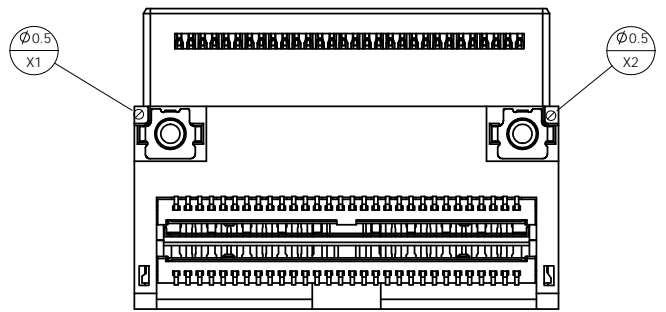
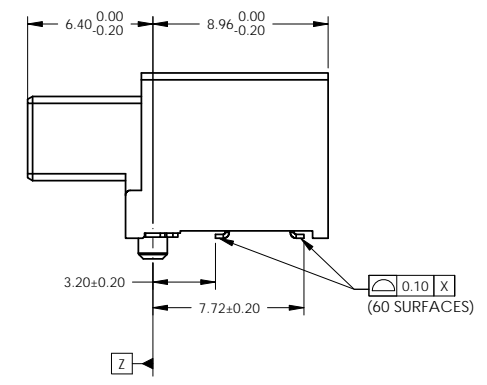
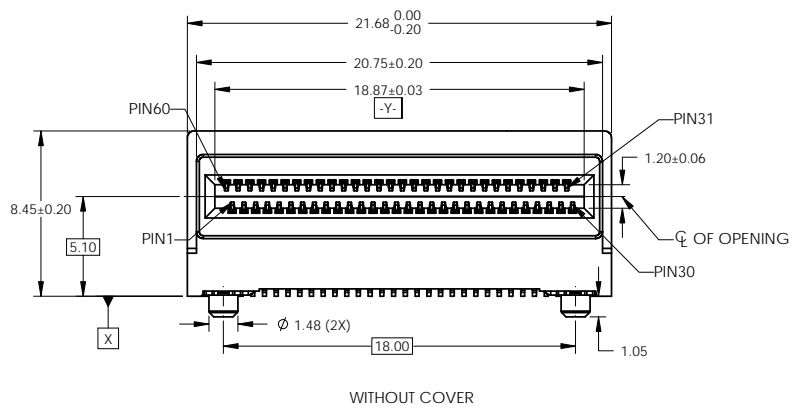
2) ROHS COMPLIANT

3) CUSTOMER NEED REMOVE THE PLASTIC PLUG AFTER SMT PROCESS AND BEFORE PRESS THE CAGE ON PCB.



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS TOLERANCES ARE: DECIMALS ANGLES XX ± 0.25 ± 0.5° X.XX ± 0.13	<b>APPROVALS</b>	<b>DATE</b>	<b>Amphenol High Speed Interconnects</b> A Division of Amphenol Corp. www.amphenol-highspeed.com
	DRAWN Mike.H	DEC. 25, 18	
MATERIAL AND FINISH	CHECKED Chigow.X	DEC. 25, 18	TITLE
SEE NOTE 1	QA APPD		60PINS, 0.60 PITCH, RIGHT ANGLE 1X1 OSFP SMT 112G CONNECTOR
REF.	E APPD		SIZE DWG. NO. P-UE62-A1020-XX00X REV. G
CODE IDENT NO. 03554	DWG APPD J.SI	DEC. 25, 18	SCALE 2:1 PROJECT OSFP SHEET 1 OF 4

REVISIONS				
REV.	ECN	DESCRIPTION	DATE	APPROVED
C		ADD REVERSED PACKING OPTION	JUN.12.20	J.SI
D		UPDATED DRAWING	OCT.14.20	J.SI
E		ADD PLUG AS COVER	MAR.01.21	J.SI
F		REMOVED COVER	OCT.21.22	J.SI
G		ADD THE HOUSING UL NUMBER	SEP.04.23	J.SI



**DO NOT SCALE DRAWING**

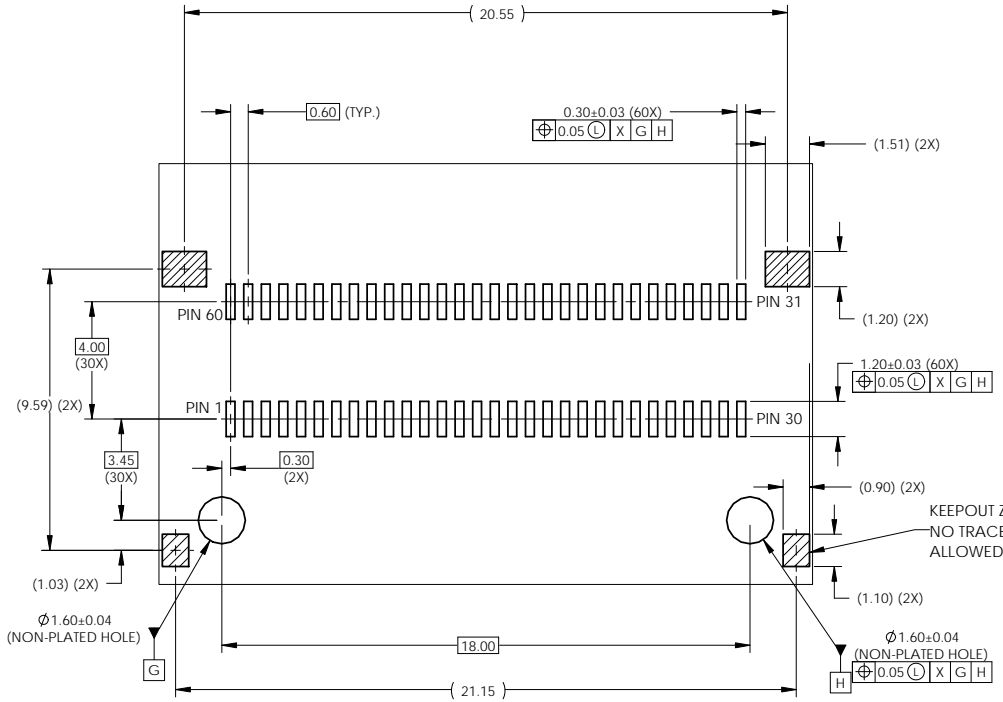
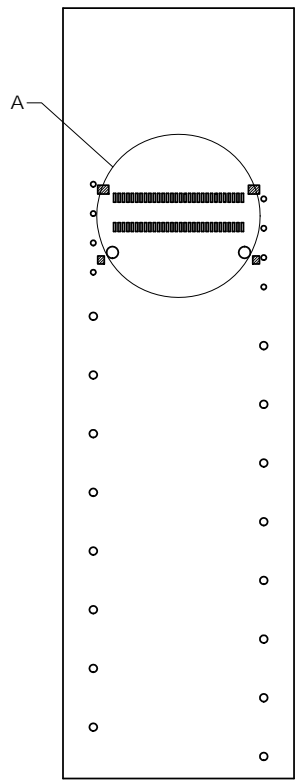
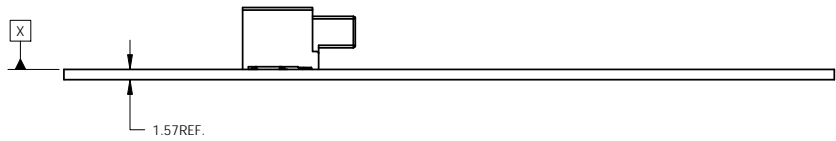
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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS TOLERANCES ARE: DECIMALS ANGLES X.X ± 0.25 ± 0.5° X.XX ± 0.13	<b>APPROVALS</b>	<b>DATE</b>	<b>Amphenol High Speed Interconnects</b> A Division of Amphenol Corp. www.amphenol-highspeed.com		
	DRAWN Mike.H	DEC. 25, 18			
MATERIAL AND FINISH SEE NOTE 1	CHECKED Chigow.X	DEC. 25, 18	TITLE 60PINS, 0.60 PITCH, RIGHT ANGLE 1X1 OSFP SMT 112G CONNECTOR		
REF.	QA APPD				
CODE IDENT NO: 03554	DWG APPD J.SI	DEC. 25, 18	SIZE A3	DWG. NO. P-UE62-A1020-XX00X	REV. G
			SCALE 2:1	PROJECT OSFP	SHEET 2 OF 4

NOTES:

- PIN ASSIGNMENT SEE PINOUT TABLE FOR DETAILS.
- PIN 15, PIN16, PIN45 AND PIN46 ARE POWER PINS.  
PIN15 AND PIN16 CONNECTED, PIN45 AND PIN46 CONNECTED.
- PIN ASSIGNMENT SEE OSFP MSA FOR DETAIL.

REVISIONS				
REV.	ECN	DESCRIPTION	DATE	APPROVED
C		ADD REVERSED PACKING OPTION	JUN.12.20	J.SI
D		UPDATED DRAWING	OCT.14.20	J.SI
E		ADD PLUG AS COVER	MAR.01.21	J.SI
F		REMOVED COVER	OCT.21.22	J.SI
G		ADD THE HOUSING UL NUMBER	SEP.04.23	J.SI



KEEPOUT ZONE IN HATCHED AREAS  
NO TRACES OR INK MARKING  
ALLOWED

DETAIL A  
SCALE 6 : 1  
RECOMMENDED PCB LAYOUT  
FOR SINGLE SIDE MOUNTING

DO NOT SCALE DRAWING

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DECIMALS X.X ± 0.25 X.XX ± 0.13		ANGLES ± 0.5°		DRAWN Mike.H		DEC. 25, 18	
MATERIAL AND FINISH SEE NOTE 1		CHECKED Chigow.X		DESIGNED		TITLE 60PINS, 0.60 PITCH, RIGHT ANGLE 1X1 OSFP SMT 112G CONNECTOR	
REF.		E APPD		GIA APPD		SCALE A3 1:1	
CODE IDENT NO: 03554		DWG APPD J.SI		DEC. 25, 18		DWG. NO. P-UE62-A1020-XX00X	
						REV. G	
						PROJECT OSFP	
						SHEET 3 OF 4	

