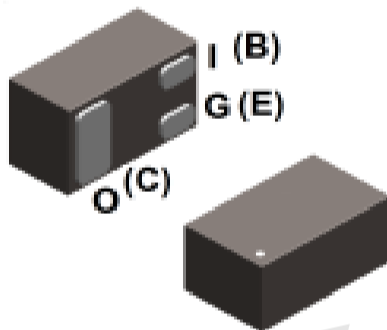


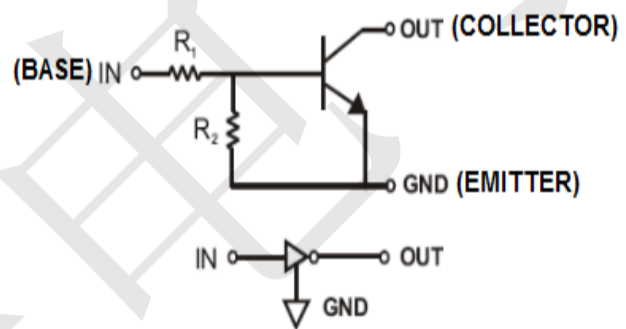
Features

- Epitaxial planar die construction
- Built-in biasing resistors (R_1 : 10k Ω , R_2 : 10k Ω)
- Also available in lead free version
- RoHS compliant with Halogen-free



Mechanical Data

- Case: DFN1006-3
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208



Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
		DFN1006-3	
Supply Voltage	V_{CC}	50	V
Input Voltage	V_I	-10 to +40	V
Output Current	I_O	50	mA
Collector Current	$I_{C(\text{Max})}$	100	mA
Power Dissipation	P_D	100	mW
Junction Temperature Range	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input Voltage	$V_{I(\text{OFF})}$	$V_{CC} = 5\text{V}, I_o = 100\mu\text{A}$	0.5	-	-	V
Input Voltage	$V_{I(\text{ON})}$	$V_o = 0.3\text{V}, I_o = 10\text{mA}$	-	-	3	V
Output Voltage	$V_{O(\text{on})}$	$I_o = 10\text{mA}, I_i = 0.5\text{mA}$	-	-	0.3	V
Input Current	I_i	$V_i = 5\text{V}$	-	-	0.88	mA
Output Current	$I_{O(\text{off})}$	$V_{CC} = 50\text{V}, V_i = 0\text{V}$	-	-	0.5	μA
DC Current Gain	G_i	$V_o = 5\text{V}, I_o = 5\text{mA}$	30	-	-	-
Input Resistor	R_i		7	10	13	k Ω
Resistance ratio	R_2/R_1		0.8	1.0	1.2	-
Gain-Bandwidth Product	f_T	$V_{CE} = 10\text{V}, I_E = 5\text{mA}$ $f = 100\text{MHz}$	-	250	-	MHz

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

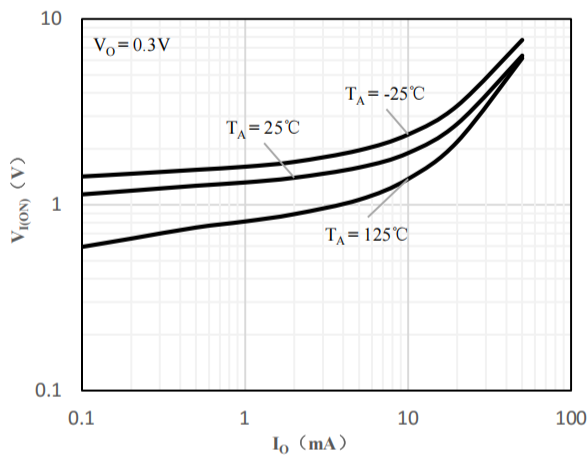


Fig 1 Input Voltage vs Output Current

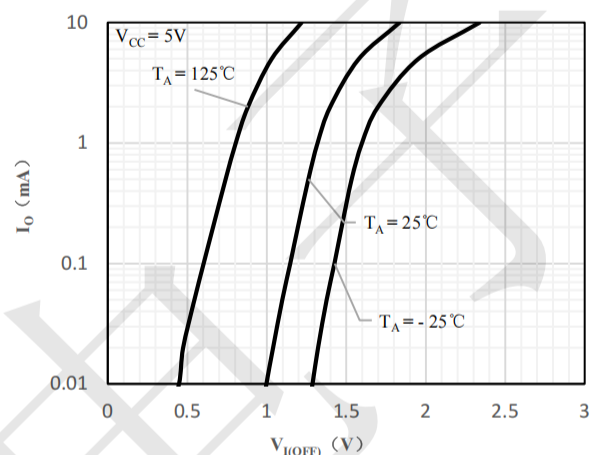


Fig 2 Output Current vs Input Voltage

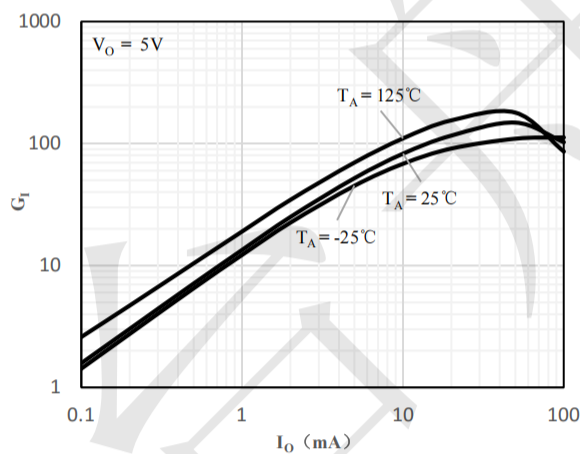


Fig 3 DC Current Gain vs Output Current

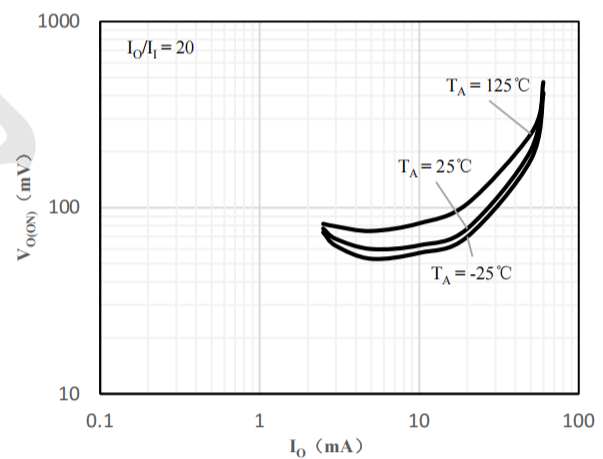
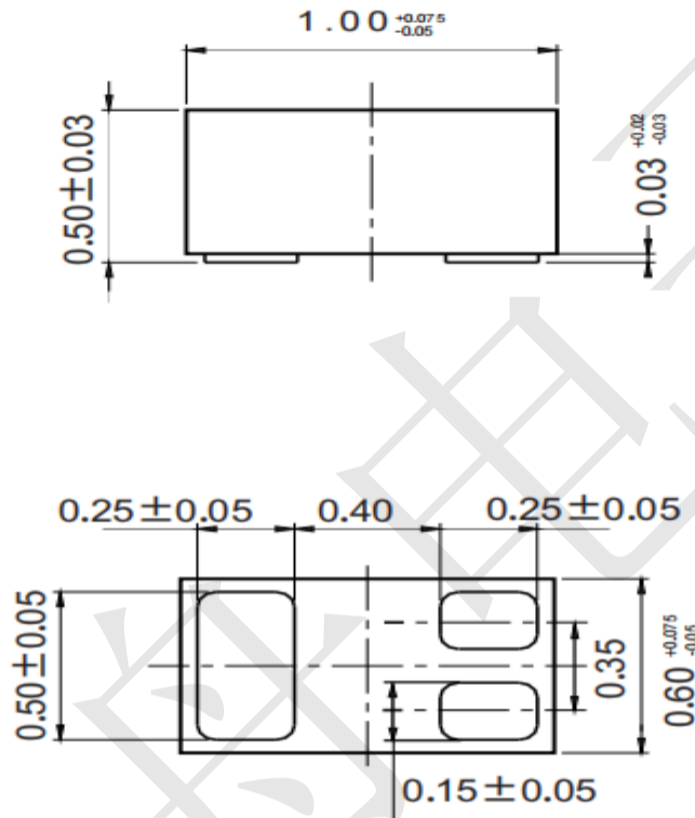


Fig 4 Output Voltage vs Output Current

Package Outline Dimensions (unit:mm)

DFN1006-3



Mounting Pad Layout (unit: mm)

