

FDMA905P-VB Datasheet

P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
- 20	0.020 at $V_{GS} = -10$ V	- 10 ^a	20 nC
	0.028 at $V_{GS} = -4.5$ V	- 8 ^a	
	0.040 at $V_{GS} = -2.5$ V	- 6	

FEATURES

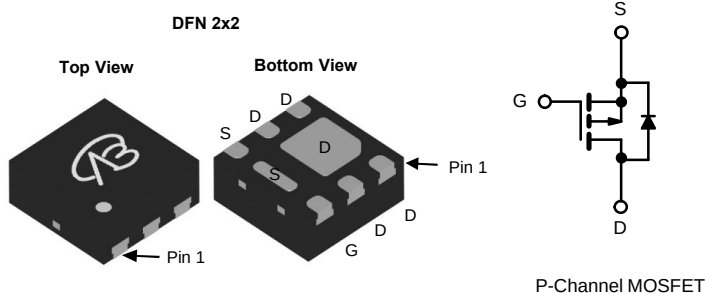
- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Portable Devices
 - Load Switch
 - Battery Switch
 - Charger Switch



ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	- 20	V
Gate-Source Voltage		V_{GS}	± 12	
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	$T_C = 25^\circ\text{C}$	I_D	- 10 ^a	
	$T_C = 70^\circ\text{C}$		- 7 ^a	
	$T_A = 25^\circ\text{C}$		- 8 ^{b, c}	A
	$T_A = 70^\circ\text{C}$		- 7.1 ^{b, c}	
Pulsed Drain Current		I_{DM}	- 30	A
Continuous Source-Drain Diode Current	$T_C = 25^\circ\text{C}$	I_S	- 6 ^a	
	$T_A = 25^\circ\text{C}$		- 2.9 ^{b, c}	
Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	19	W
	$T_C = 70^\circ\text{C}$		12	
	$T_A = 25^\circ\text{C}$		3.5 ^{b, c}	
	$T_A = 70^\circ\text{C}$		2.2 ^{b, c}	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$
Soldering Recommendations (Peak Temperature) ^{d, e}			260	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, e}		R_{thJA}	28	36	$^\circ\text{C/W}$
Maximum Junction-to-Case (Drain)		R_{thJC}	5.3	6.5	

Notes:

a. Package limited.

b. Surface Mounted on 1" x 1" FR4 board.

c. $t = 5$ s.

d. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

e. Maximum under Steady State conditions is 80°C/W .

SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 20			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 12		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			3		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA		- 0.6		V
Gate-Source Leakage	I _{GSS}	V _{DS} = -20 V, V _{GS} = ± 12 V			± 20	μA
		V _{DS} = -20 V, V _{GS} = ± 4.5 V			± 0.5	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≤ - 5 V, V _{GS} = - 4.5 V	- 20			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 5.6 A		0.020		Ω
		V _{GS} = - 4.5 V, I _D = - 5.3 A		0.028		
		V _{GS} = - 2.5 V, I _D = - 2.5 A		0.040		
Forward Transconductance ^a	g _{fs}	V _{DS} = - 10 V, I _D = - 5.6 A		35		S
Dynamic ^b						
Total Gate Charge	Q _g	V _{DS} = - 10 V, V _{GS} = - 8 V, I _D = - 5 A		50	75	nC
Gate-Source Charge		V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = - 5 A		20	30	
				3.3		
Gate-Drain Charge	Q _{gd}			8.4		
Gate Resistance	R _g	f = 1 MHz	0.2	1	2	kΩ
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 10 V, R _L = 1 Ω I _D ≅ - 5 A, V _{GEN} = - 4.5 V, R _g = 1 Ω		0.71	1.1	μs
Rise Time	t _r			1.7	2.6	
Turn-Off Delay Time	t _{d(off)}			6	9	
Fall Time	t _f			3.2	5	
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 10 V, R _L = 1 Ω I _D ≅ - 5 A, V _{GEN} = - 10 V, R _g = 1 Ω		0.3	0.45	
Rise Time	t _r			0.6	0.9	
Turn-Off Delay Time	t _{d(off)}			10	15	
Fall Time	t _f			3.5	5.5	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 10	A
Pulse Diode Forward Current	I _{SM}				- 30	
Body Diode Voltage	V _{SD}	I _S = - 5 A, V _{GS} = 0 V		- 0.85	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 6 A, dI/dt = 100 A/μs, T _J = 25 °C		30	60	ns
Body Diode Reverse Recovery Charge	Q _{rr}			20	40	nC
Reverse Recovery Fall Time	t _a			13		ns
Reverse Recovery Rise Time	t _b			17		

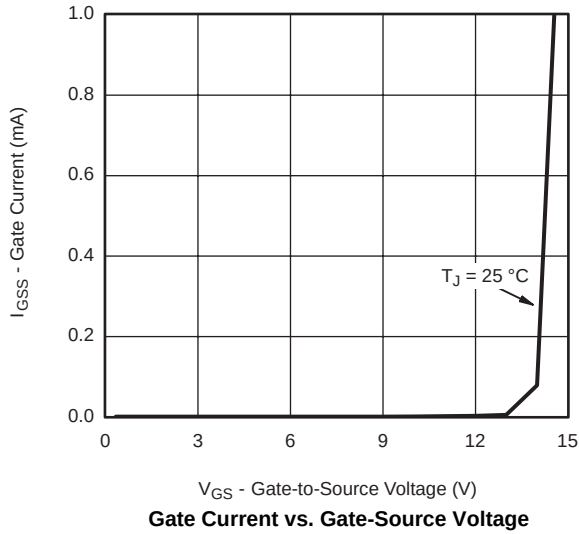
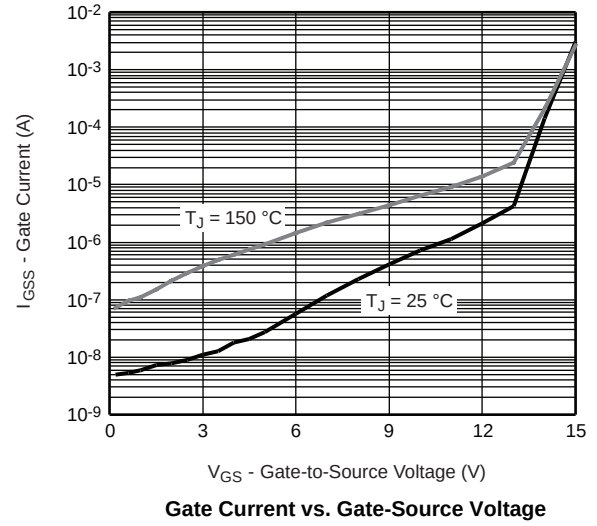
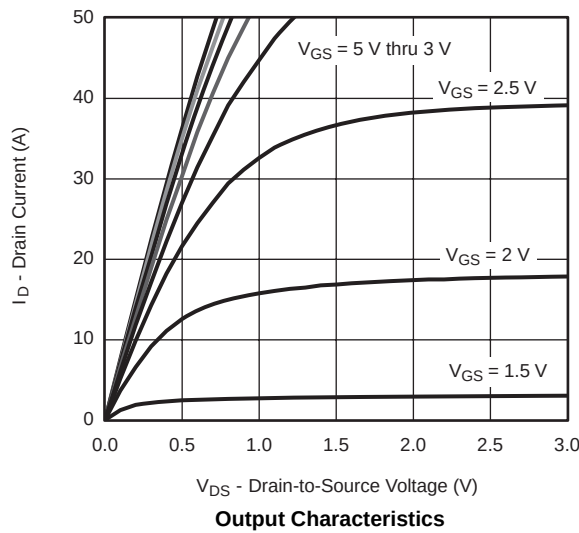
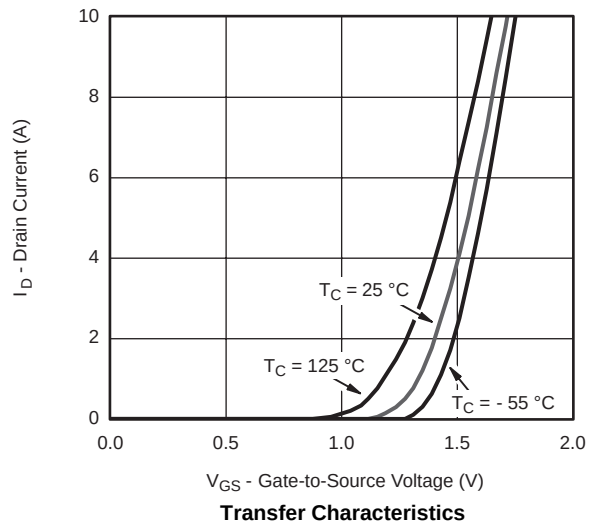
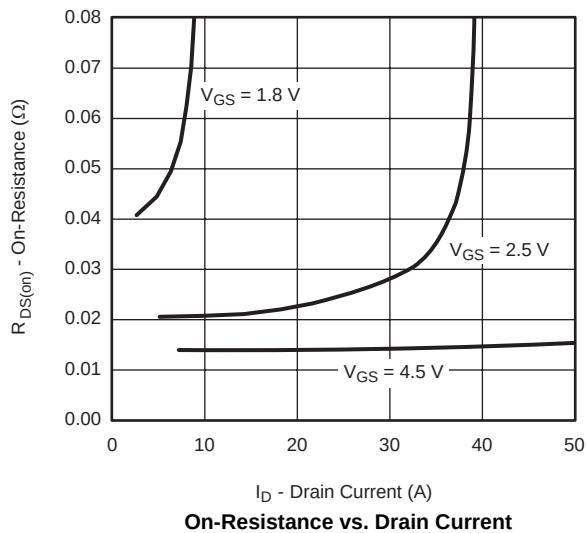
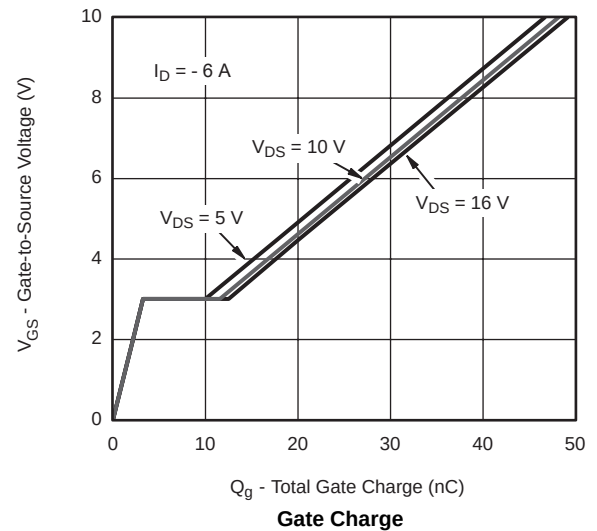
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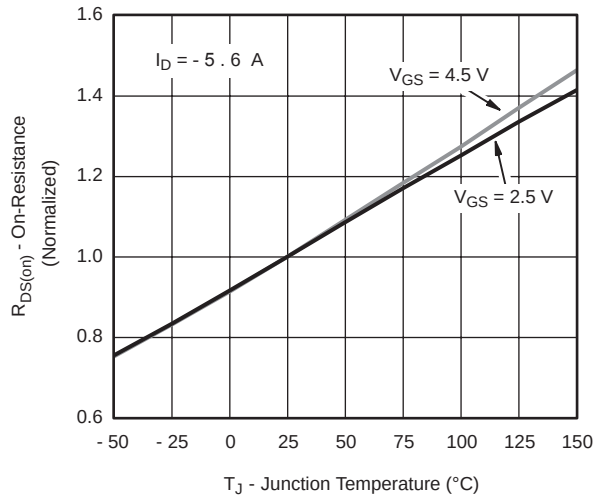
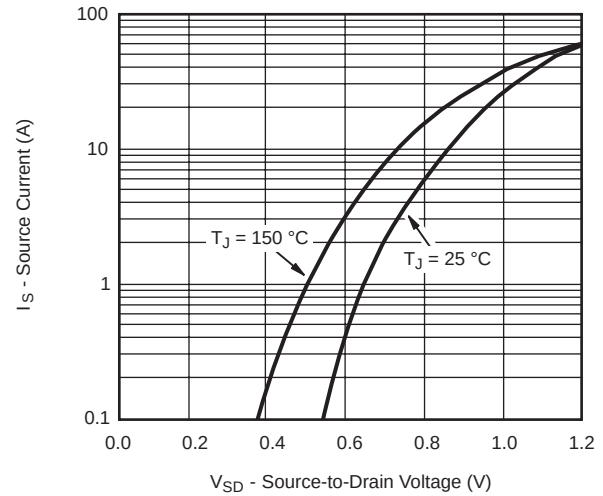
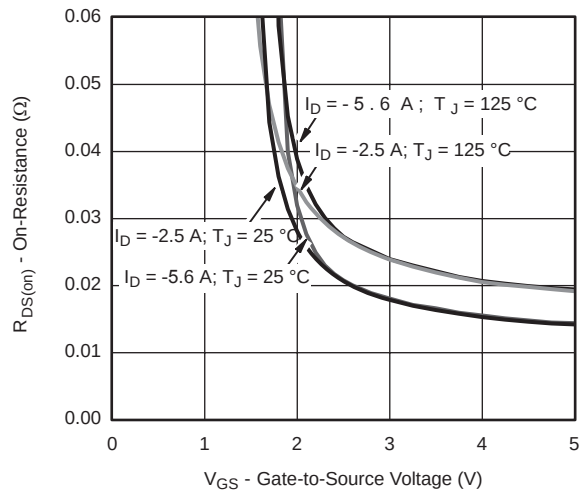
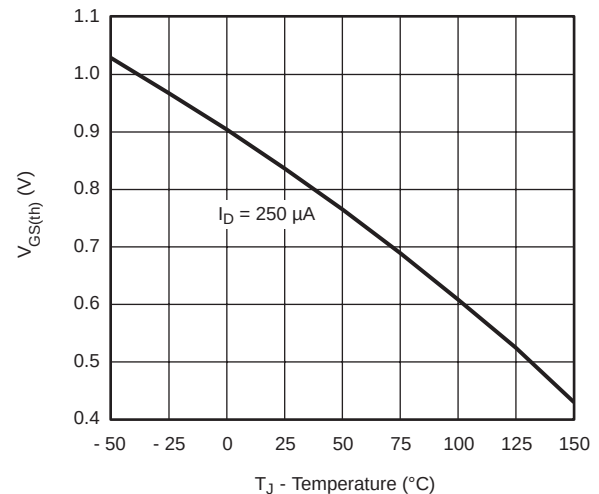
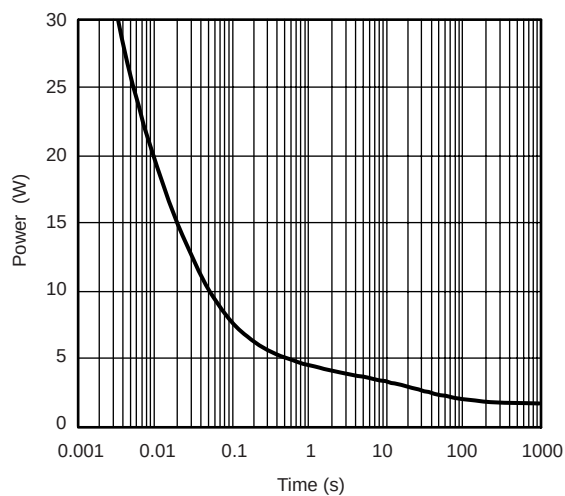
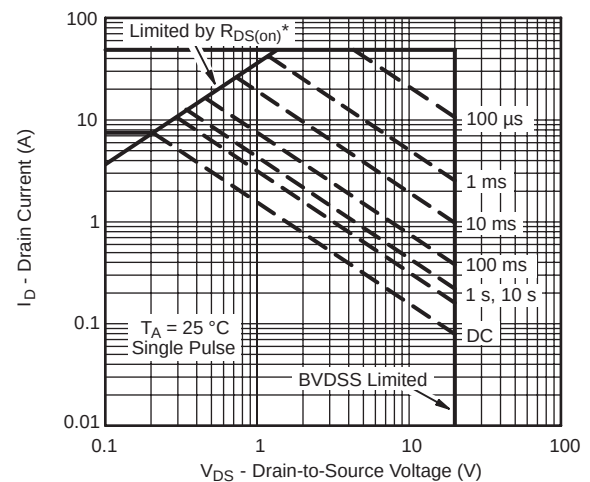
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

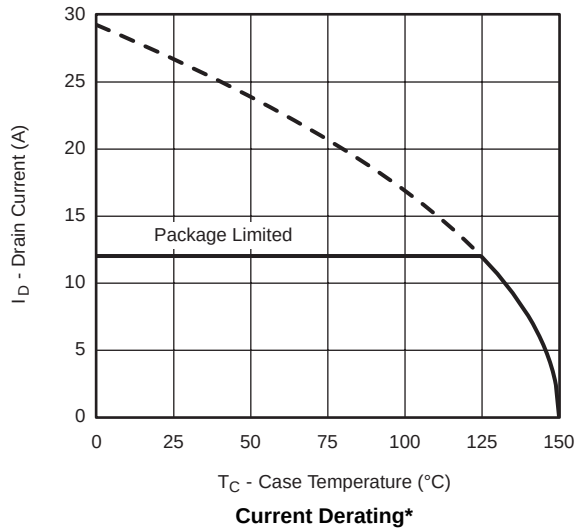
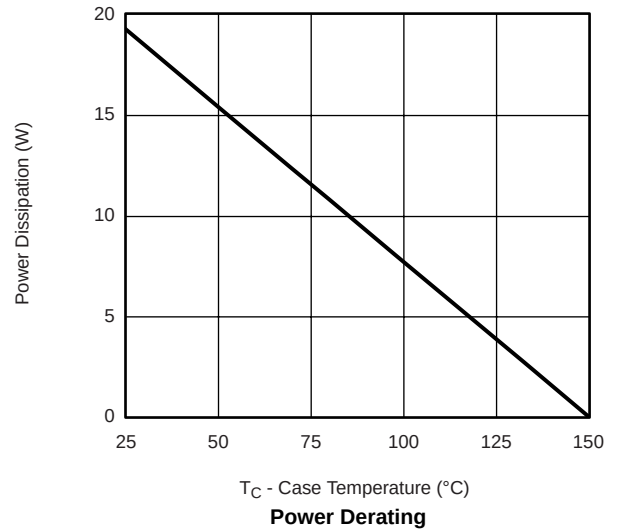
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

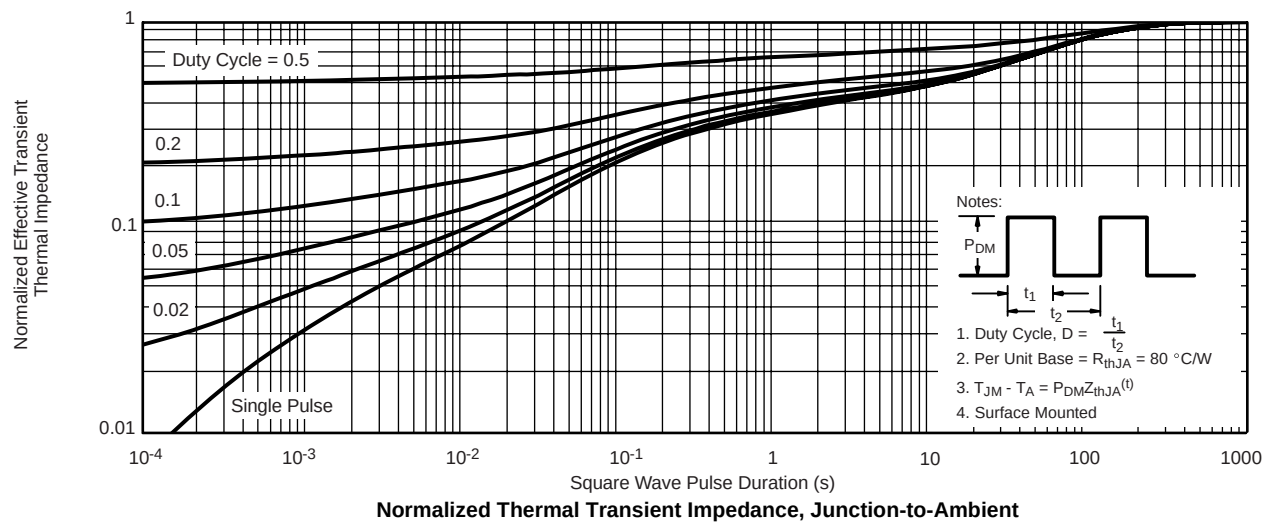
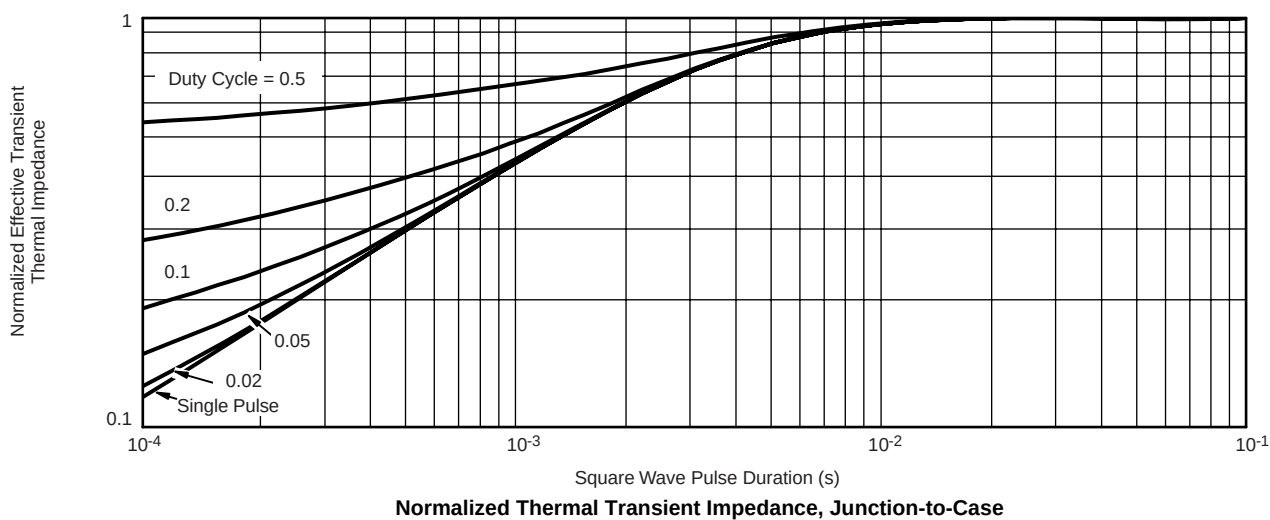
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



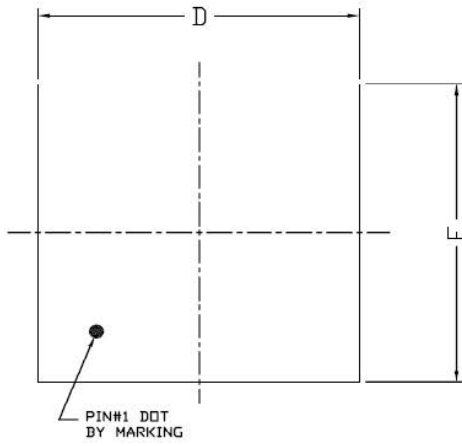
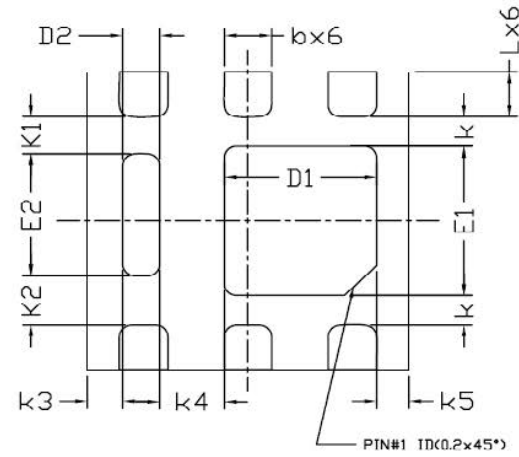
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

On-Resistance vs. Junction Temperature

Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

Threshold Voltage

Single Pulse Power, Junction-to-Ambient

Safe Operating Area, Junction-to-Ambient
 * $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

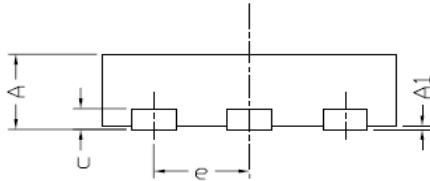
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


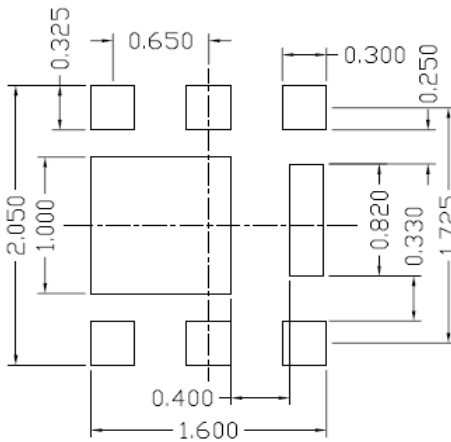
DFN2x2 _6L_EP1_S PACKAGE OUTLINE



BOTTOM VIEW



RECOMMENDED LAND PATTERN



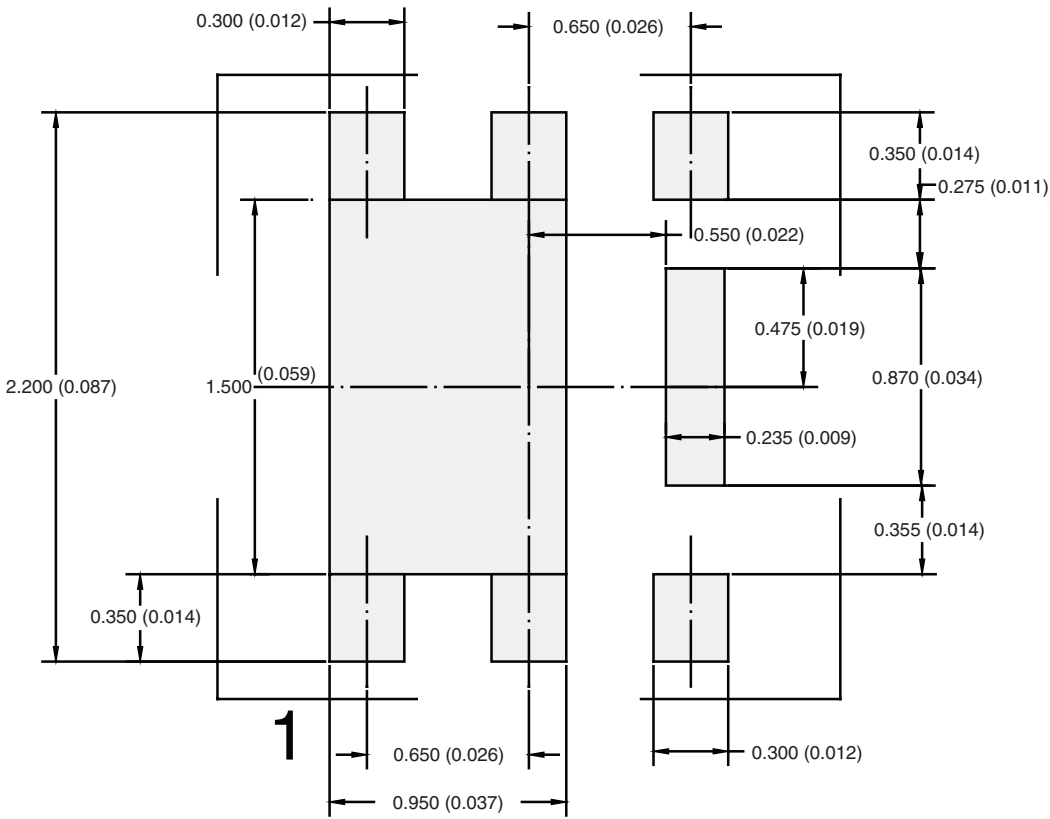
UNIT: mm

NOTE

1. CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.022	0.024
A1	0.00	—	0.05	0.000	—	0.002
b	0.25	0.30	0.35	0.010	0.012	0.014
c	0.152 REF			0.006 REF		
D	1.90	2.00	2.10	0.075	0.079	0.083
D1	0.85	0.95	1.05	0.033	0.037	0.041
D2	0.13	0.23	0.33	0.005	0.009	0.013
E	1.90	2.00	2.10	0.075	0.079	0.083
E1	0.90	1.00	1.10	0.035	0.039	0.043
E2	0.72	0.82	0.92	0.028	0.032	0.036
e	0.65 BSC			0.026 BSC		
K	0.20 BSC			0.008 BSC		
K1	0.25 BSC			0.010 BSC		
K2	0.33 BSC			0.013 BSC		
K3	0.22 BSC			0.009 BSC		
K4	0.40 BSC			0.016 BSC		
K5	0.20 BSC			0.008 BSC		
L	0.25	0.30	0.35	0.010	0.012	0.014

RECOMMENDED PAD LAYOUT FOR DFN2X2



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