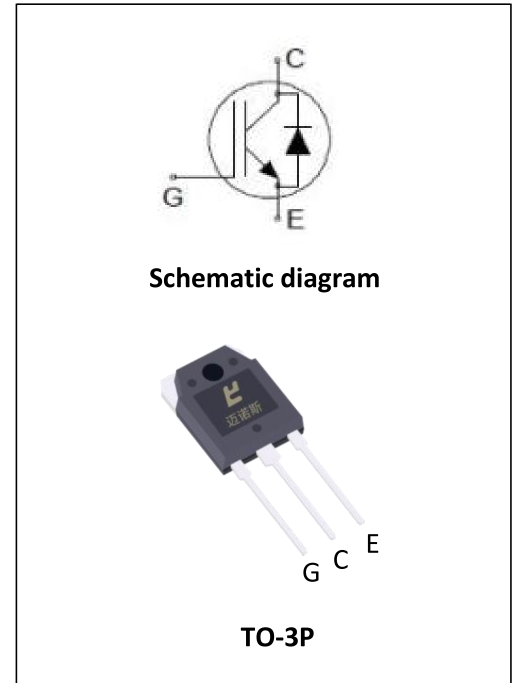
迈诺斯科技

MLG40N60FDL-W

Description

MLG40N60FDL-W is obtained by advanced Trench Field Stop (T-FS) technology which is characteristic with low $V_{CE(sat)}$, optimized switching performance and low gate charge Q_g. The IGBT is suitable device for Photovoltaic, UPS and high switching frequency applications.



Schematic diagram

TO-3P

General Features

1. 600V Breakdown Voltage
2. Fast Switching
3. Low V_{CE}(sat),typ=1.45V@I_C=40A and T_C=25℃
4. Positive temperature coefficient
5. Fast recovery anti-parallel diode
6. RoHS product

APPLICATIONS

1. Photovoltaic converters
2. UPS

ORDERING INFORMATION

Ordering Codes	Package	Product Code	Packing
MLG40N60FDL-W	TO-3P	G40N60FDL	Tube

ABSOLUTE RATINGS

Symbol	Parameter	TO-3P	Units
V_{CES}	Collector-Emitter Voltage	600	V
I_C	Collector Current @TC=25°C	80	A
	Collector Current @TC=100°C	40	A
I_{CM}	Pulsed Collector Current, tp limited by TJmax	160	A
I_F	Diode Continuous Forward Current @TC=25°C	80	A
	Diode Continuous Forward Current @TC=100 °C	40	A
I_{FM}	Diode Maximum Forward Current, limited by TJmax	160	A
V_{GES}	Gate-Emitter Voltage	±30	V
t_{SC}	Short circuit withstand time VGE=15V, VCC≤400V, Allowed number of short circuits<1000,Times between short circuits: ≥ 1.0s,TJ ≤ 175°C	8.0	µs
P_D	Power Dissipation @TC=25°C	300	W
T_{Jmax}, T_{stg}	Operating Junction and Storage Temperature Range	175, –55 to 175	℃
T_L	Maximum Temperature for Soldering	260	℃

Thermal characteristics

Symbol	Parameter	TO-3P	Units
$R_{θJC}$	Junction-to-Case (IGBT)	0.5	℃/W
$R_{θJC}$	Junction-to-Case (Diode)	0.65	℃/W
$R_{θJA}$	Junction-to-Ambient	40	℃/W

Electrical Characteristics（at TC = 25°C, unless otherwise specified）

Static Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
V_{CES}	Collector-Emitter Breakdown Voltage	V_{GE} = 0V, I_C = 250μA	600	--	--	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V_{GE} = 15V,I_C = 40A T_J=25℃ T_J=125℃ T_J=175℃	-- -- --	1.45 1.65 1.75	1.85 -- --	V
$V_{GE(TH)}$	Gate Threshold Voltage	V_{CE} = V_{GE} ,I_C = 1mA	4.7	5.5	6.2	V
V_F	Diode Forward Voltage	I_F=40A,T_J=25℃ T_J=125℃ T_J=175℃	-- -- --	2.20 1.80 1.60	2.90 -- --	V
I_{CES}	Collector-Emitter Leakage Current	V_{CE} = 600V, V_{GE} = 0V	--	--	35	μA
$I_{GES(F)}$	Gate-Emitter Forward Leakage Current	V_{GE} = +30V	--	--	200	nA
$I_{GES(R)}$	Gate-Emitter Reverse Leakage Current	V_{GE} = -30V	--	--	-200	nA
Pulse width tp≤300μs, δ≤2%						

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	V_{GE} =0V V_{CE} =25V f =1.0MHz	--	2125	--	pF
C_{oss}	Output Capacitance		--	157	--	
C_{rss}	Reverse Transfer Capacitance		--	24	--	
Q_G	Gate charge	V_{CC}=520V I_{CE}=20A V_{GE} =15V	--	110	--	nC
Q_{GE}	Gate-emitter charge		--	55	--	
Q_{GC}	Gate-collector charge		--	22	--	
$I_{C(SC)}$	Short circuit collector current Max.1000 short circuits, Times between short circuits:	V_{GE} =15.0V,V_{CC}≤400V， t_{SC}≤8us， T_J≤175℃	--	250	--	A

IGBT Switching Characteristics, at TJ=25℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	I_C =40A V_{CE} = 400V V_{GE} = 15V R_G =5Ω T_J=25°C Inductive Load	--	20	--	ns
t_r	Rise Time		--	33	--	
$t_{d(off)}$	Turn-Off Delay Time		--	112	--	
t_f	Fall Time		--	66	--	
E_{on}	Turn-On Switching Loss		--	0.65	--	mJ
E_{off}	Turn-Off Switching Loss		--	0.68	--	
E_{ts}	Total Switching Loss		--	1.33	--	

IGBT Switching Characteristics, at TJ=175℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	I_C =40A V_{CE} = 400V V_{GE} = 15V R_G =5Ω T_J=175°C Inductive Load	--	19	--	ns
t_r	Rise Time		--	34	--	
$t_{d(off)}$	Turn-Off Delay Time		--	148	--	
t_f	Fall Time		--	112	--	
E_{on}	Turn-On Switching Loss		--	0.87	--	mJ
E_{off}	Turn-Off Switching Loss		--	0.89	--	
E_{ts}	Total Switching Loss		--	1.76	--	

Diode Characteristics, at TJ=25℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
T_{rr}	Reverse Recovery Time	I_F=20A, di/dt=200A/µs, T_J=25°C	--	38	--	ns
Q_{rr}	Reverse Recovery Charge		--	81	--	nC
I_{rrm}	Reverse Recovery Current		--	2.8	--	A
T_{rr}	Reverse Recovery Time	I_F=40A, di/dt=200A/µs, T_J=25°C	--	45	--	ns
Q_{rr}	Reverse Recovery Charge		--	97	--	nC
I_{rrm}	Reverse Recovery Current		--	3.5	--	A

Diode Characteristics, at TJ=175℃

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
T_{rr}	Reverse Recovery Time	I_F=20A, di/dt=200A/µs, T_J=175°C	--	112	--	ns
Q_{rr}	Reverse Recovery Charge		--	130	--	nC
I_{rrm}	Reverse Recovery Current		--	3.3	--	A
T_{rr}	Reverse Recovery Time	I_F=40A, di/dt=200A/µs, T_J=175°C	--	151	--	ns
Q_{rr}	Reverse Recovery Charge		--	163	--	nC
I_{rrm}	Reverse Recovery Current		--	4.6	--	A

Characteristics Curves

Figure 1. Forward Bias Safe Operating Area for TO-3P	Figure 2. Power Dissipation vs Case Temperature for TO-3P

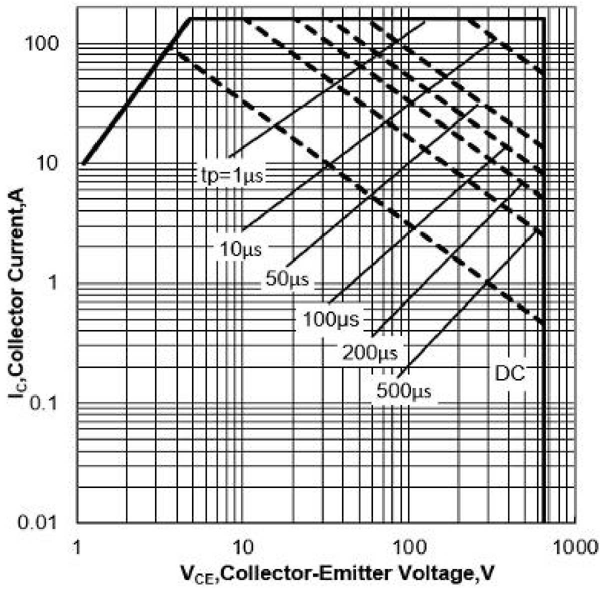
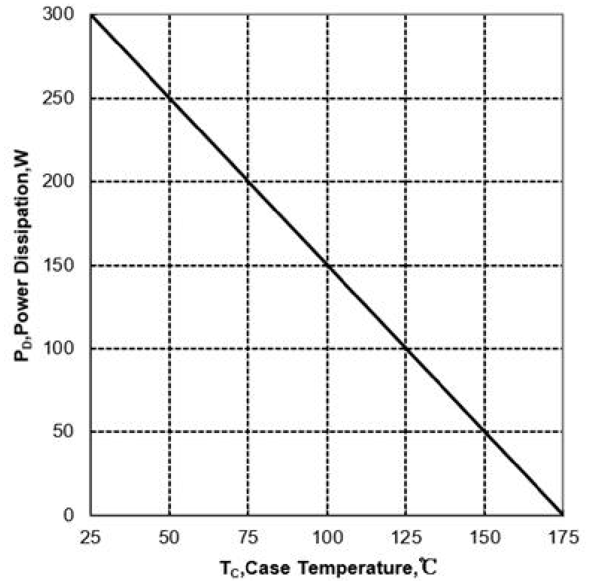

	

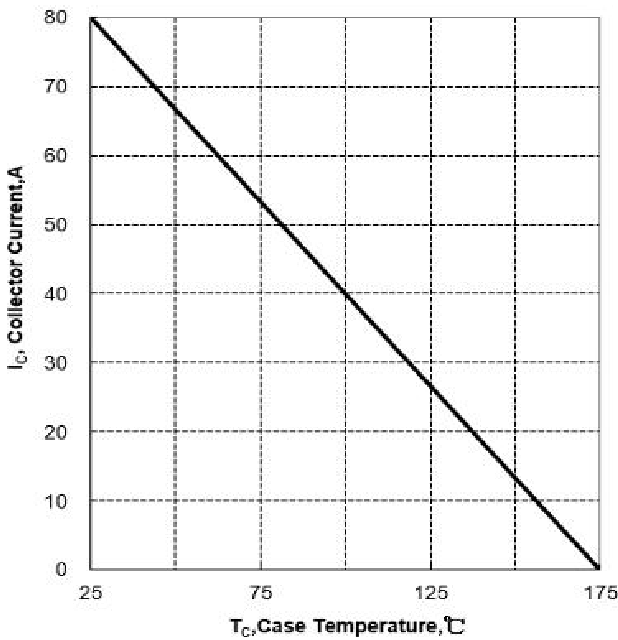
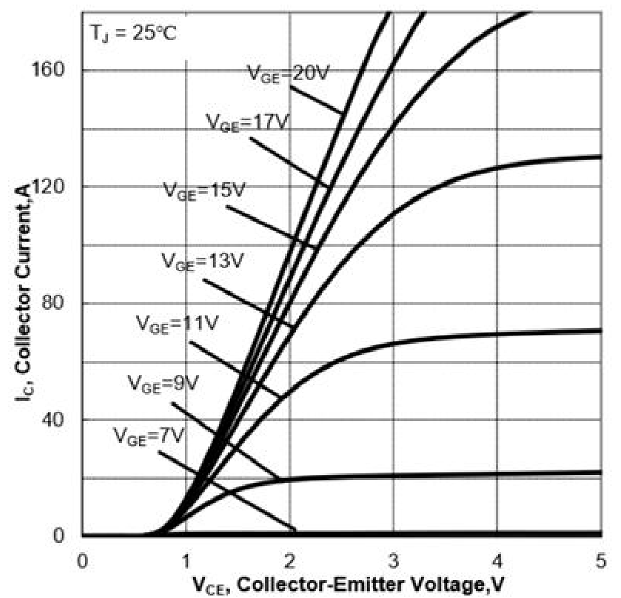
Figure 3. Collector Current vs Case Temperature for TO-3P	**Figure 4. Typical Output Characteristics (TJ=25℃)**
	

Figure 5. Typical Output Characteristics (TJ=175℃)	Figure 6. Typical Transfer Characteristics

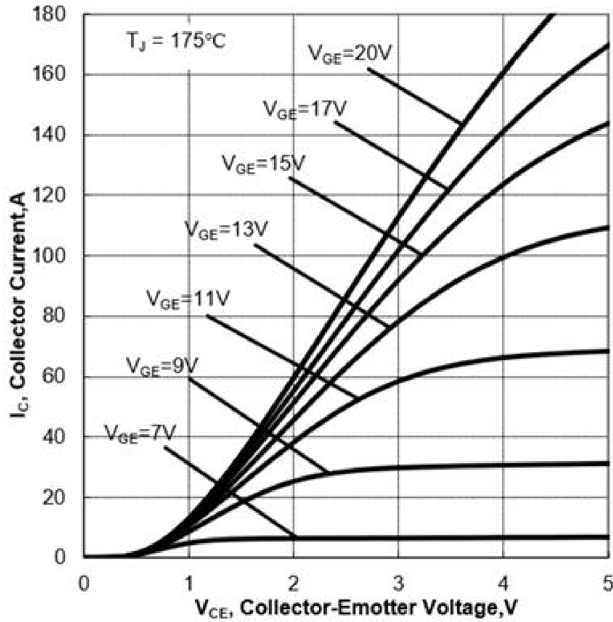
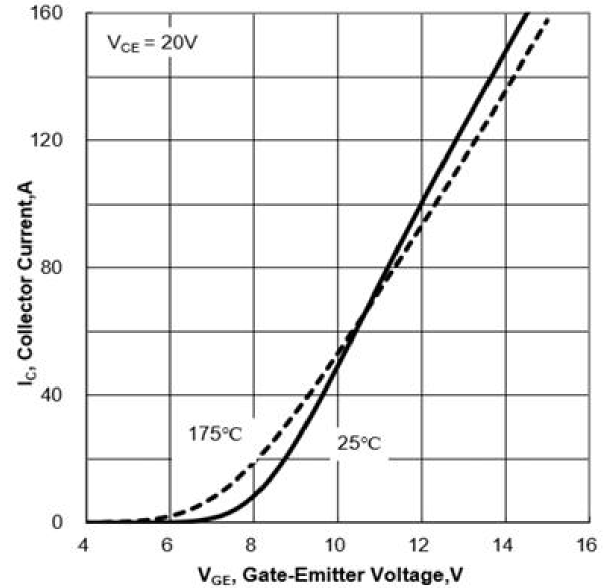

	

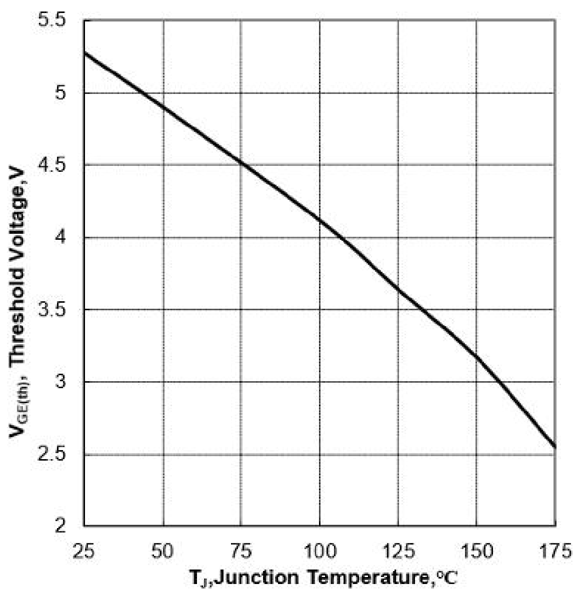
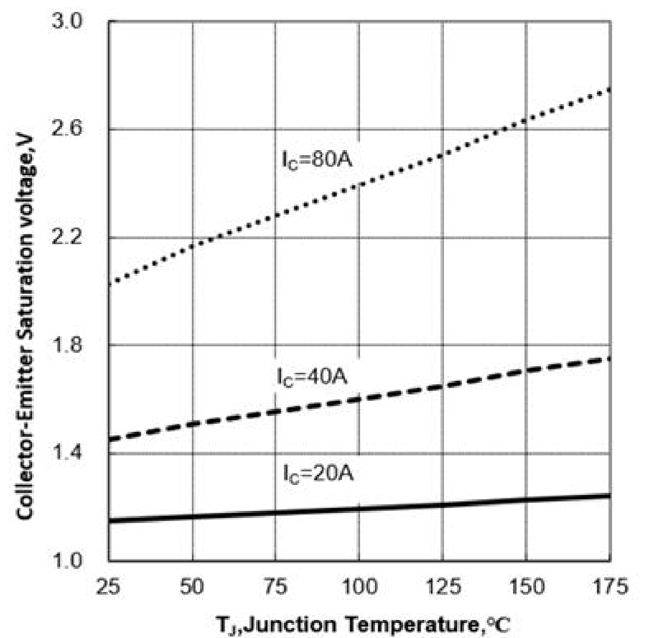
Figure 7. Typical Gate-Emitter Threshold Voltage vs Junction Temperature	**Figure 8. Typical Collector-Emitter Saturation Voltage vs Junction Temperature**
	

Figure 9. Typical Diode Forward Current vs Forward Voltage

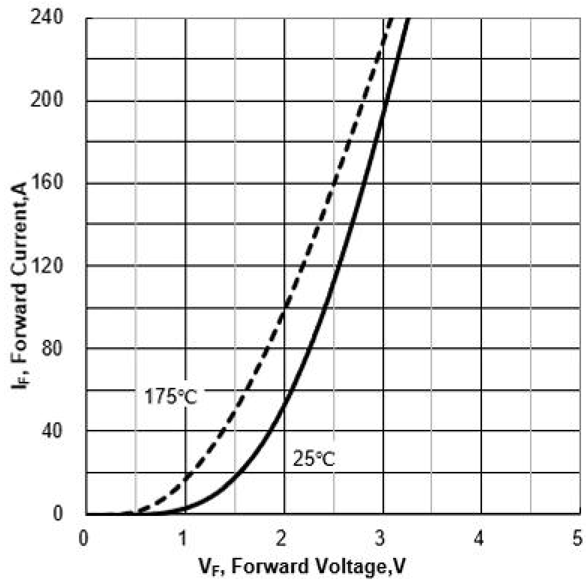


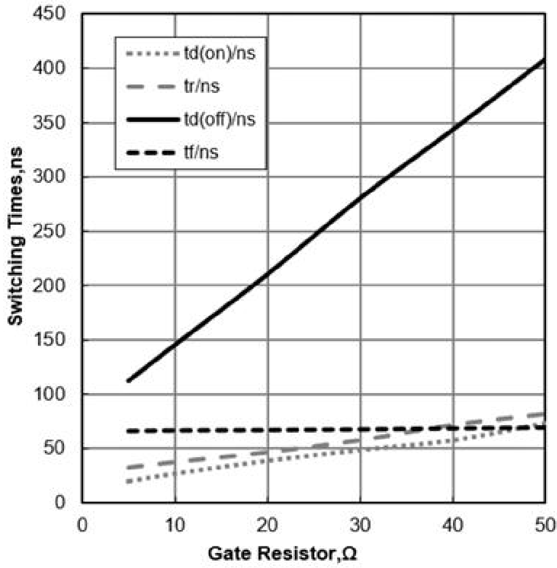
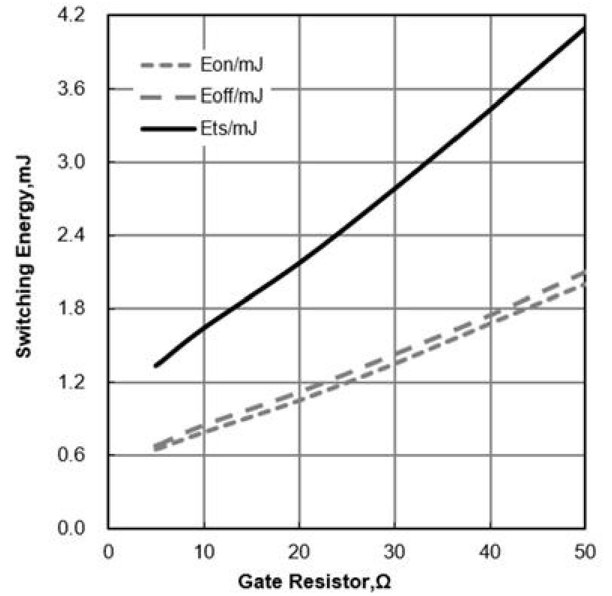
Figure 10. Typical Switching Times vs Gate Resistor (TJ=25℃, VCE=400V, VGE=15/0V, IC=40A)	Figure 11. Typical Switching Energy vs Gate Resistor (TJ=25℃, VCE=400V, VGE=15/0V, IC=40A)
	

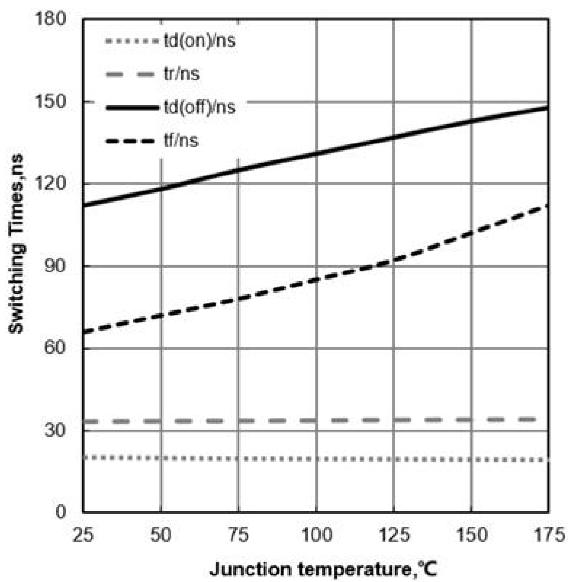
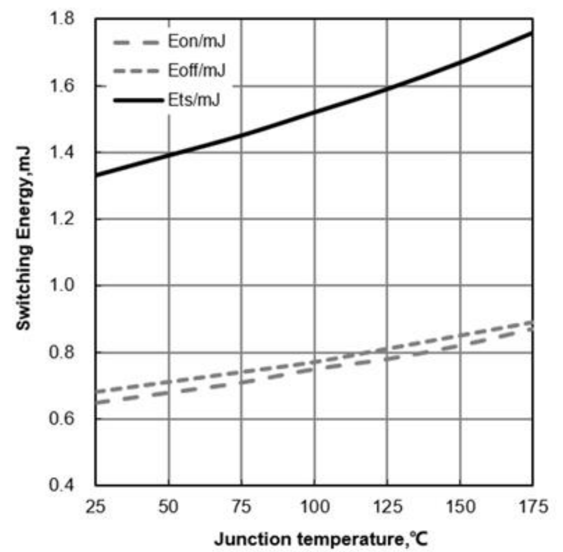
Figure 12. Typical Switching Times vs Junction Temperature (VCE=400V, VGE=15/0V, Ic=40A)	Figure 13. Typical Switching Energy vs Junction Temperature (VCE=400V, VGE=15/0V, Ic=40A)
	

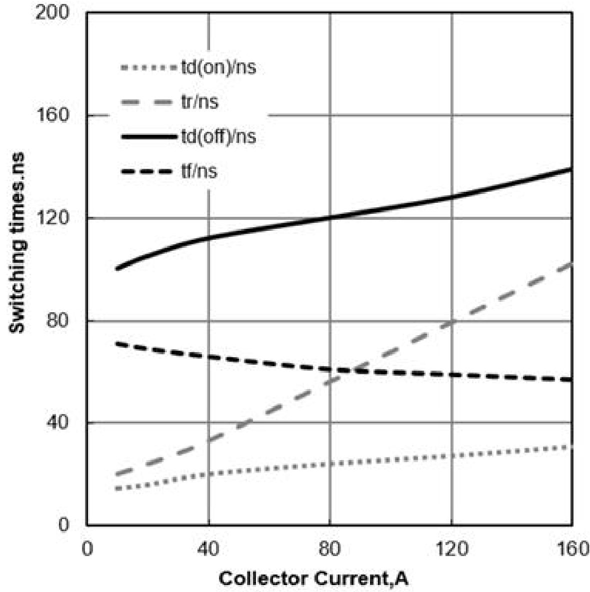
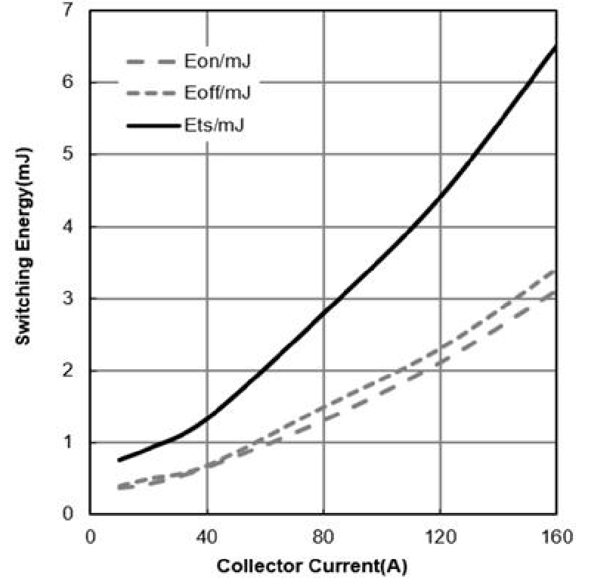
Figure 14. Typical Switching Times vs Collector Current (TJ=25℃,VCE=400V, VGE=15/0V)	Figure 15. Typical Switching Energy vs Collector Current (TJ=25℃, VCE=400V, VGE=15/0V)
	

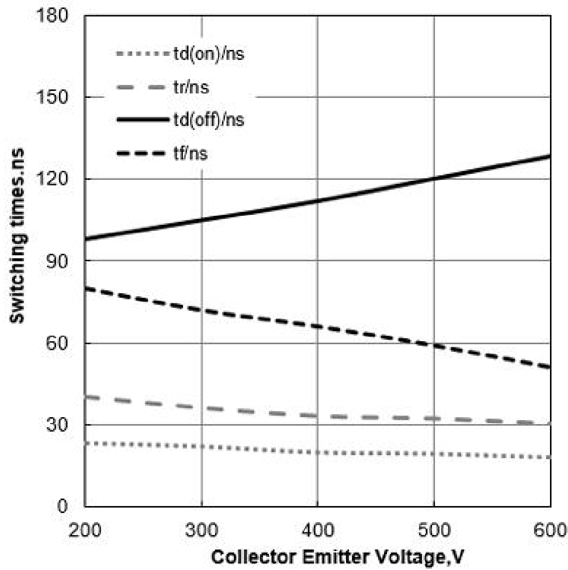
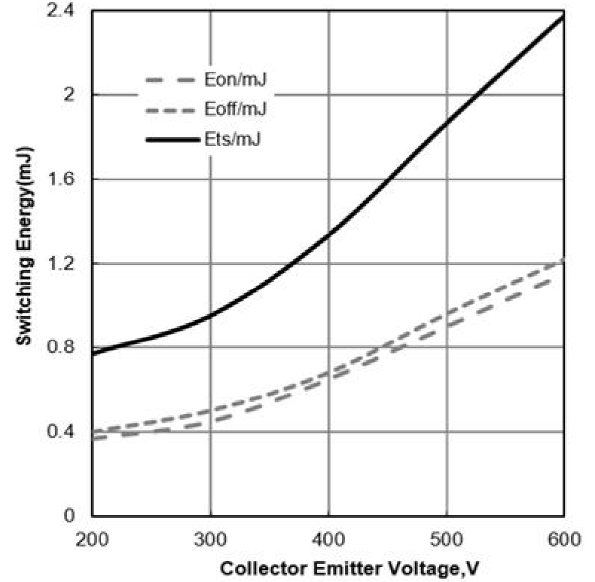
Figure 16. Typical Switching Times vs VCE (TJ=25℃,VGE=15/0V, IC=40A)	Figure 17. Typical Switching Energy vs VCE (TJ=25℃,VGE=15/0V, IC=40A)
	

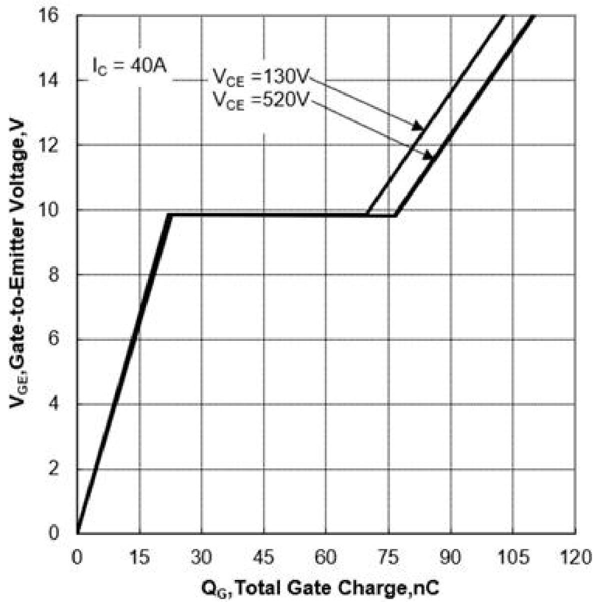
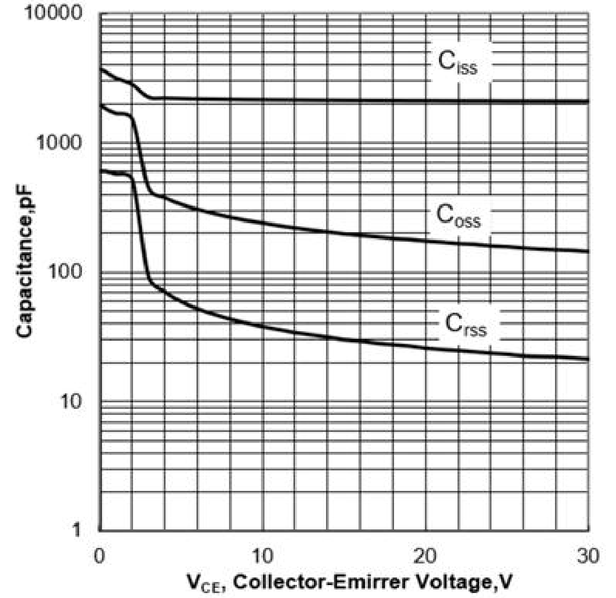
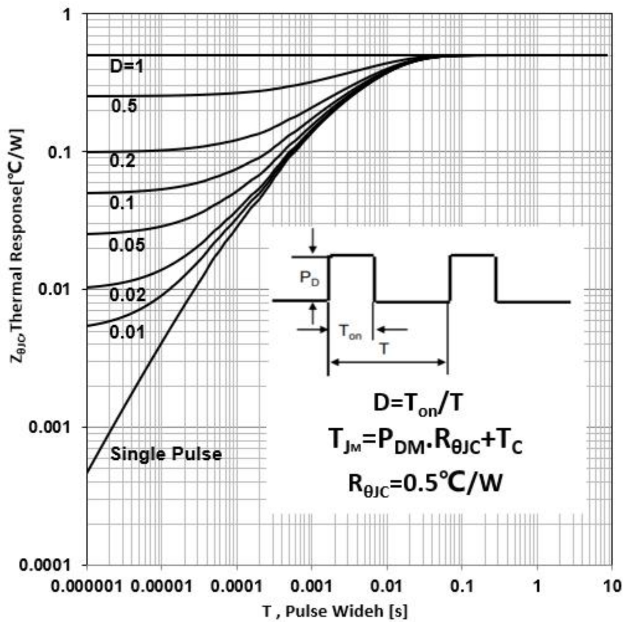
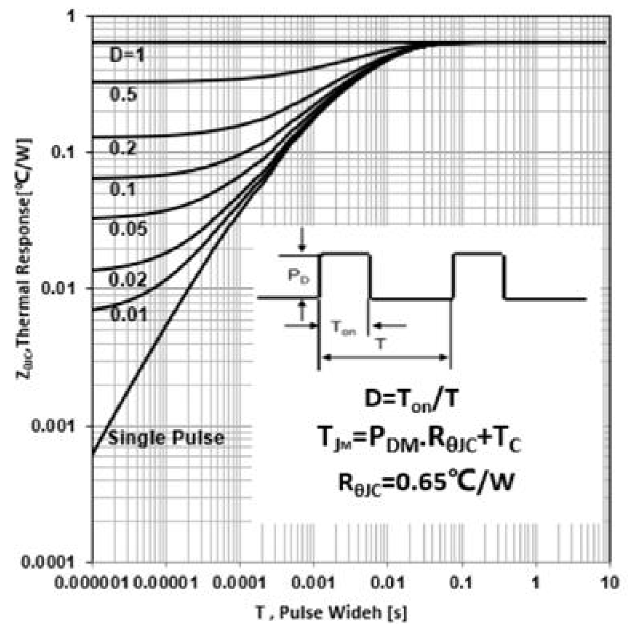
Figure 18. Typical Gate Charge	Figure 19. Typical Capacitance vs Collector- Emitter Voltage
	

Figure 20. IGBT Transient Thermal Impedance vs Pulse Width(TO-3P)	Figure 21. Diode Transient Thermal Impedance vs Pulse Width(TO-3P)
	

Test Circuit and Waveform

Figure 22. Inductive Switching Test Circuit

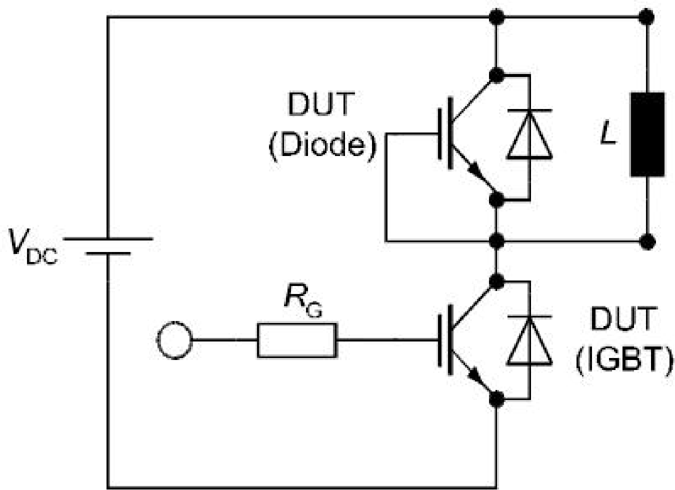


Figure 23. Definition of switching times

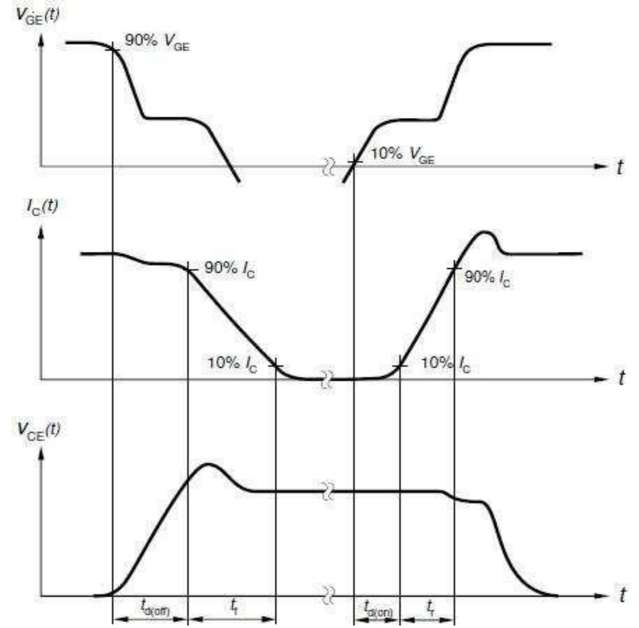


Figure 24. Definition of switching losses

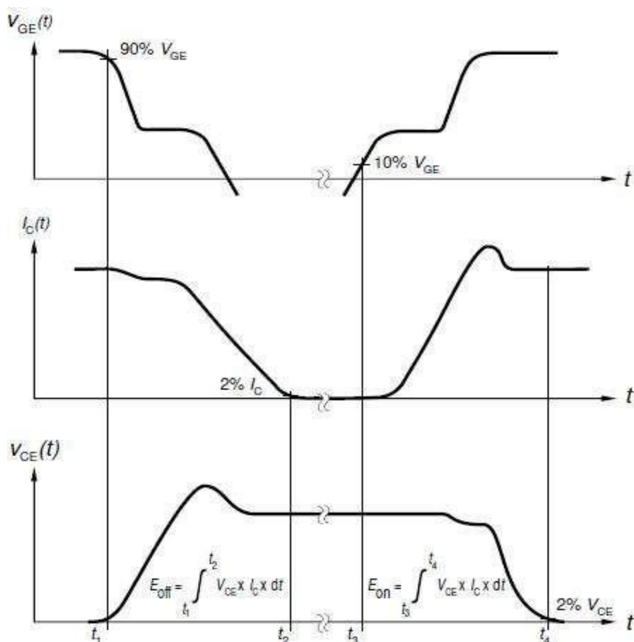
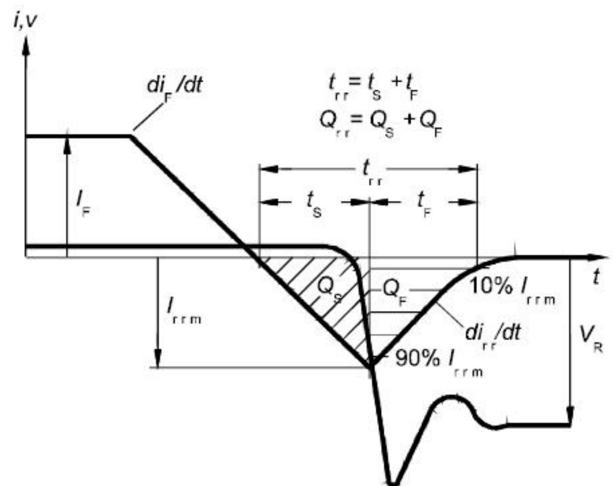
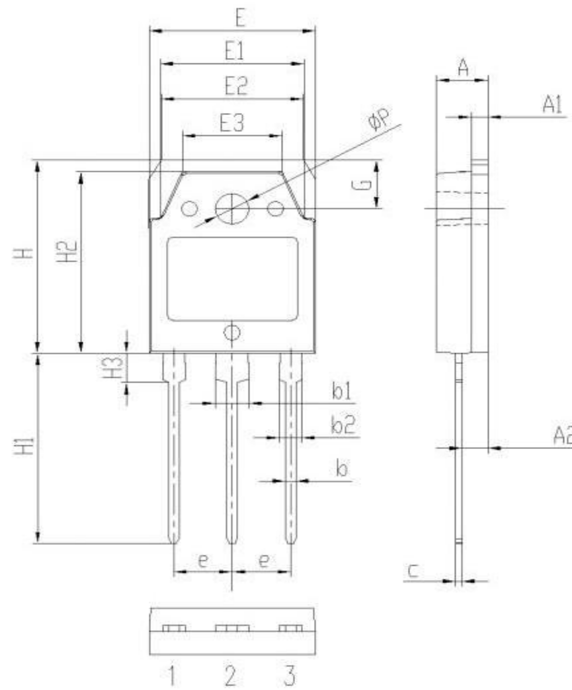


Figure 25. Definition of diode switching characteristics



Package Information



Symbol	Values(mm)	
	MIN	MAX
A	4.60	5.00
A1	1.30	1.70
A2	2.20	2.60
b	0.80	1.20
b1	2.90	3.30
b2	1.90	2.30
c	0.40	0.80
e	5.25	5.65
E	15.3	15.7
E1	13.2	13.6
E2	13.1	13.5
E3	9.10	9.50
H	19.7	20.1
H1	19.1	20.1
H2	18.3	18.7
H3	2.80	3.20
G	4.80	5.20
ϕp	3.00	3.40

TO-3P Package

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change withoutprior notice.

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