

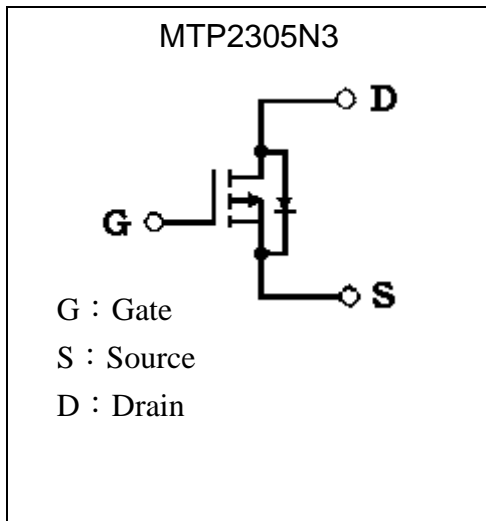
P-Channel Enhancement Mode MOSFET
MTP2305N3

BV_{DSS}	-20V
I_D@T_A=25°C, V_{GS}=-4.5V	-4.8A
R_{DS(on)}@V_{GS}=-10V, I_D=-4.5A	27mΩ (typ.)
R_{DS(on)}@V_{GS}=-4.5V, I_D=-4.2A	32mΩ (typ.)
R_{DS(on)}@V_{GS}=-2.5V, I_D=-2A	37mΩ (typ.)
R_{DS(on)}@V_{GS}=-1.8V, I_D=-1A	47mΩ (typ.)

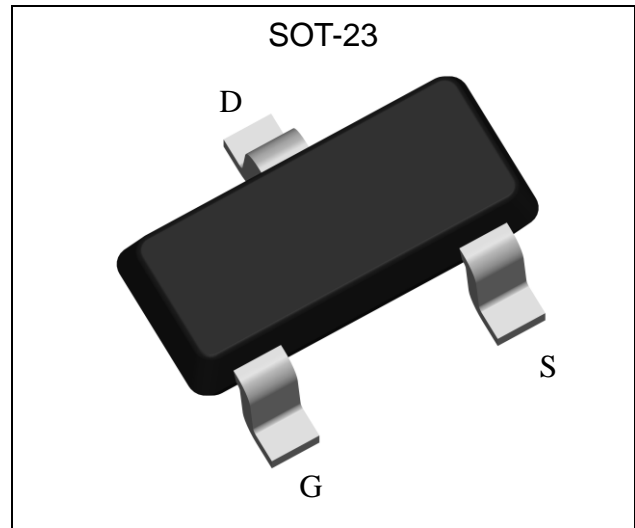
Features

- Advanced trench process technology
- Super high density cell design for extremely low on resistance
- Reliable and rugged
- Compact and low profile SOT-23 package
- Pb-free lead plating and halogen-free package

Equivalent Circuit

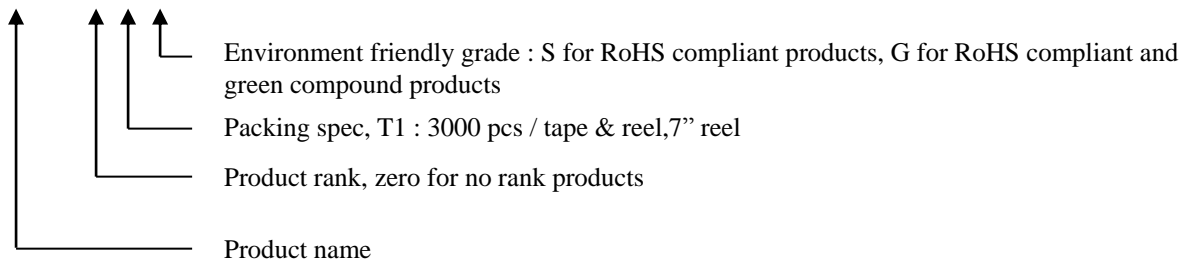


Outline



Ordering Information

Device	Package	Shipping
MTP2305N3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	±12	
Continuous Drain Current @T _A =25°C, V _{GS} =-4.5V (Note 1)	I _D	-4.8	A
Continuous Drain Current @T _A =70°C, V _{GS} =-4.5V (Note 1)		-3.8	
Pulsed Drain Current (Note 2)	I _{DM}	-20	
Maximum Power Dissipation (Note 1)	P _D	1.38	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C

Note : 1. Surface mounted on 1 in² copper pad of FR-4 board, t≤5s; 270°C/W when mounted on minimum copper pad.
 2. Pulse width limited by maximum junction temperature.

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient, max (Note)	R _{θJA}	90	°C/W
Thermal Resistance, Junction-to-Case, max	R _{θJC}	75	

Note : Surface mounted on 1 in² copper pad of FR-4 board, t≤5s ; 270°C/W when mounted on minimum copper pad.

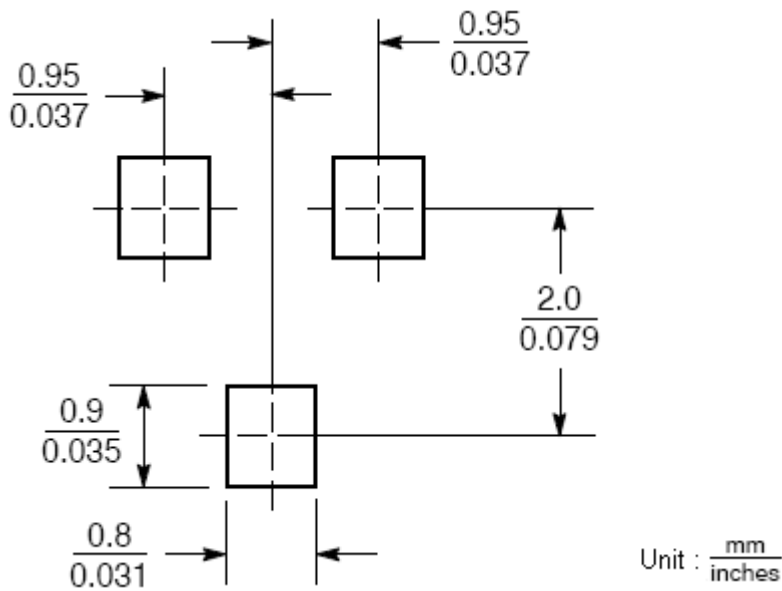
Electrical Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-20	-	-	V	V _{GS} =0V, I _D =-250μA
ΔBV _{DSS} /ΔT _j	-	-0.02	-	V/°C	Reference to 25°C, I _D =-1mA
V _{GS(th)}	-0.5	-0.65	-1.2	V	V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±12V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-20V, V _{GS} =0V
	-	-	-10		V _{DS} =-16V, V _{GS} =0V, T _j =70°C
*R _{DS(ON)}	-	27	36	mΩ	I _D =-4.5A, V _{GS} =-10V
	-	32	45		I _D =-4.2A, V _{GS} =-4.5V
	-	37	74		I _D =-2.0A, V _{GS} =-2.5V
	-	47	120		I _D =-1.0A, V _{GS} =-1.8V
*G _{FS}	-	9.5	-	S	V _{DS} =-5V, I _D =-2.8A
Dynamic					
C _{iss}	-	1139	-	pF	V _{DS} =-15V, V _{GS} =0V, f=1MHz
C _{oss}	-	99	-		
C _{rss}	-	85	-		
*t _{d(ON)}	-	4.8	-	ns	V _{DS} =-15V, I _D =-4.3A, V _{GS} =-10V, R _D =3.6Ω, R _G =6Ω
*t _r	-	17.4	-		
*t _{d(OFF)}	-	112.8	-		
*t _f	-	141.4	-		

*Qg	-	14.6	-	nC	V _{DS} =-16V, I _D =-4.3A, V _{GS} =-4.5V,
*Qgs	-	1.8	-		
*Qgd	-	4.1	-		
Source-Drain Diode					
*V _{SD}	-	-0.76	-1.2	V	V _{GS} =0V, I _{SD} =-1.2A
*trr	-	37.4	-	ns	I _F =-4.3A, V _{GS} =0V, dI _F /dt=100A/μs
*Qrr	-	16	-	nC	

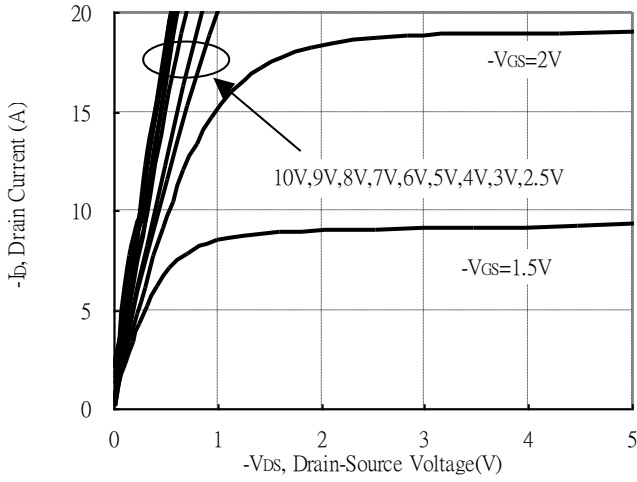
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Recommended Soldering Footprint

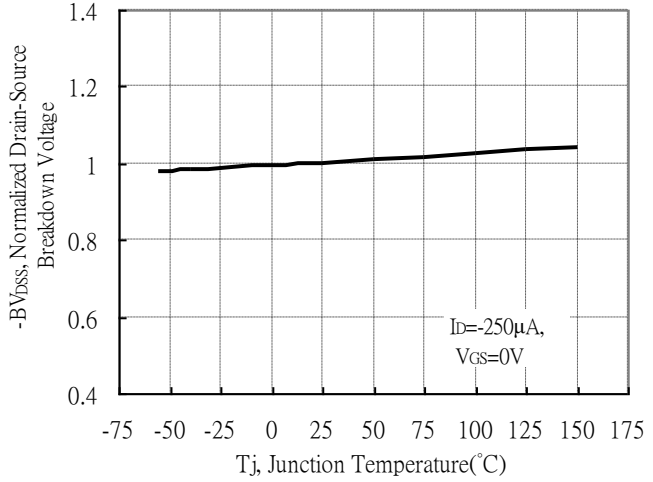


Typical Characteristics

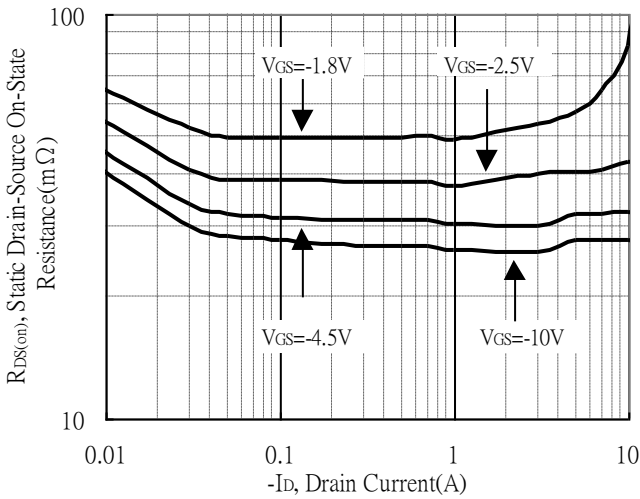
Typical Output Characteristics



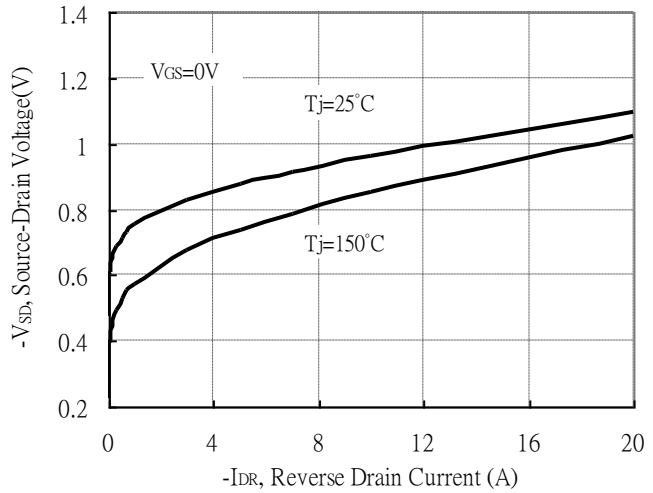
Brekdown Voltage vs Ambient Temperature



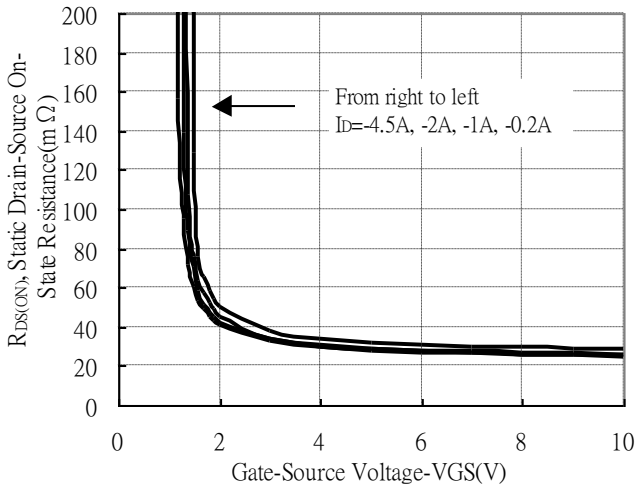
Static Drain-Source On-State resistance vs Drain Current



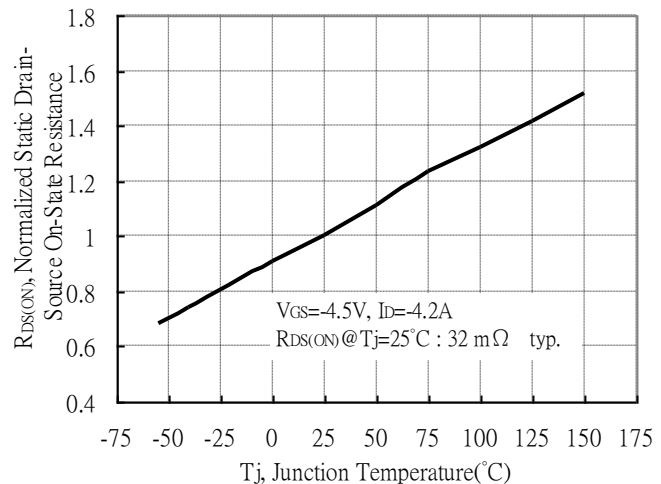
Reverse Drain Current vs Source-Drain Voltage



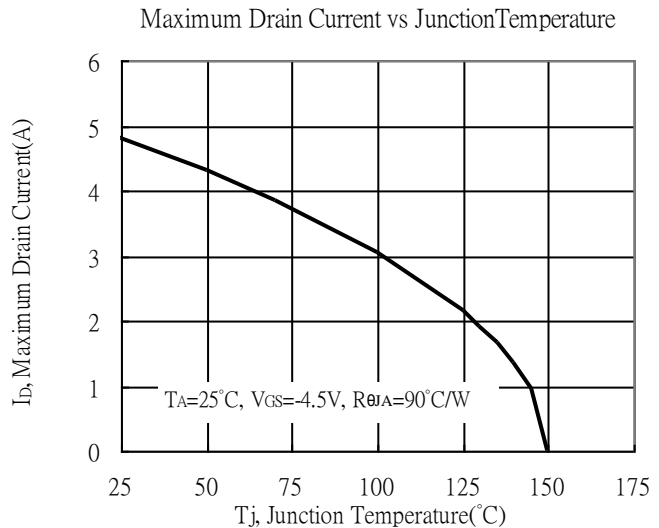
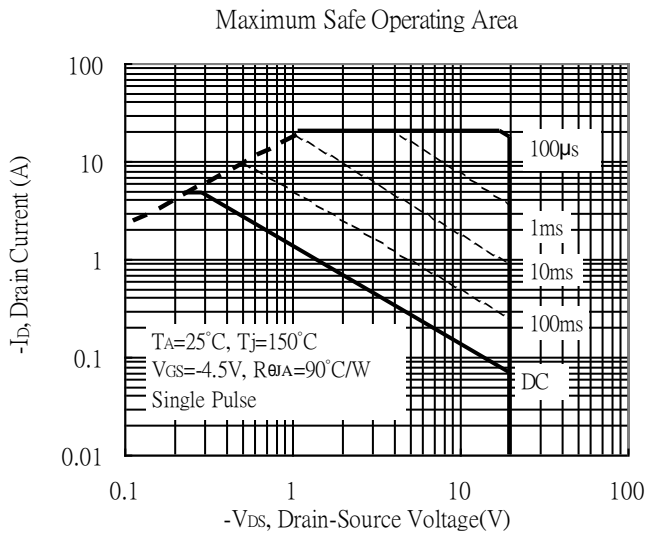
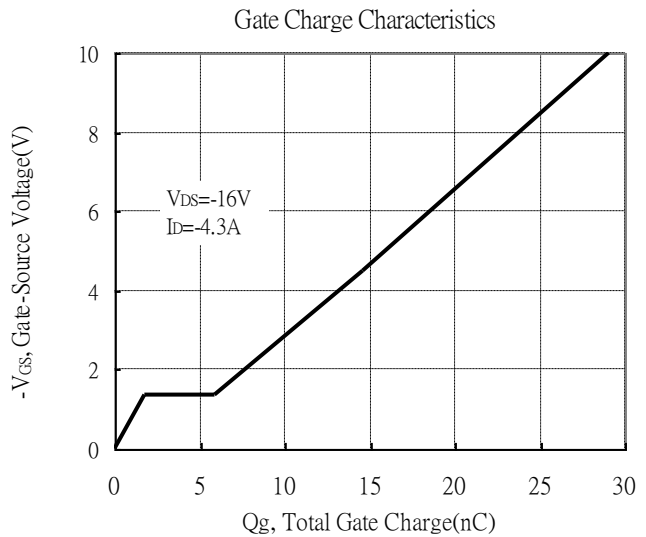
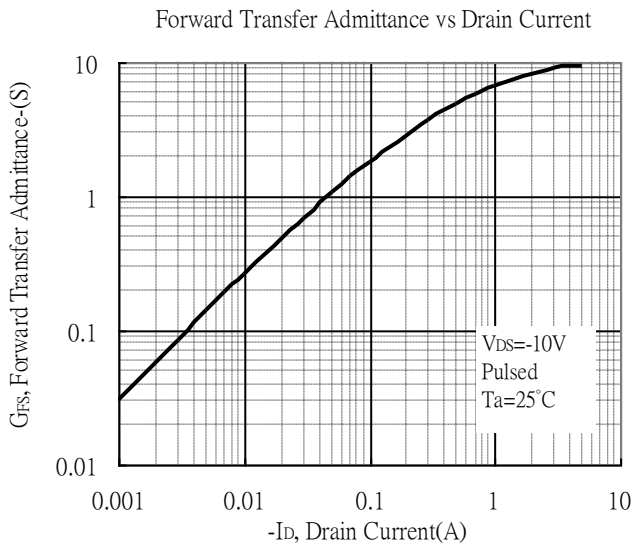
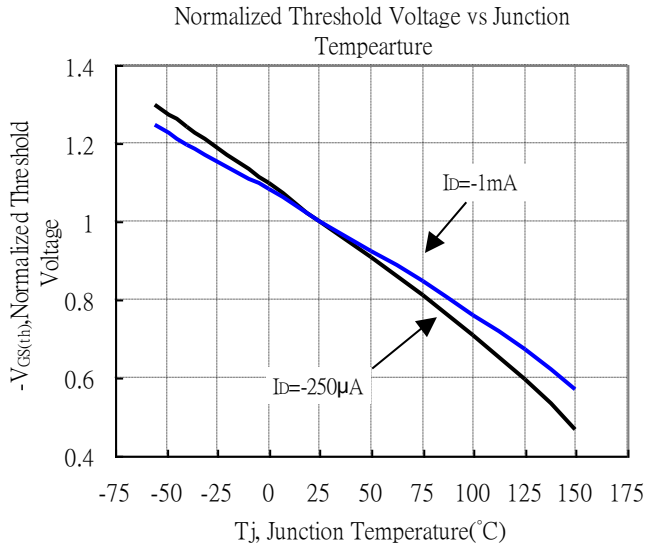
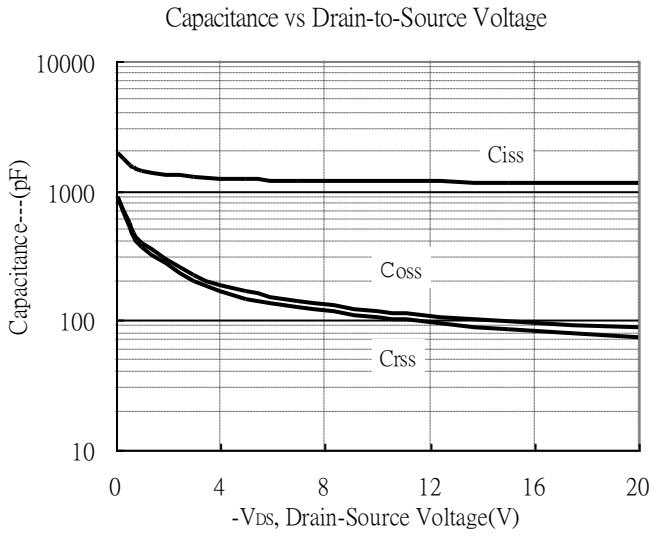
Static Drain-Source On-State Resistance vs Gate-Source Voltage



Drain-Source On-State Resistance vs Junction Temperature

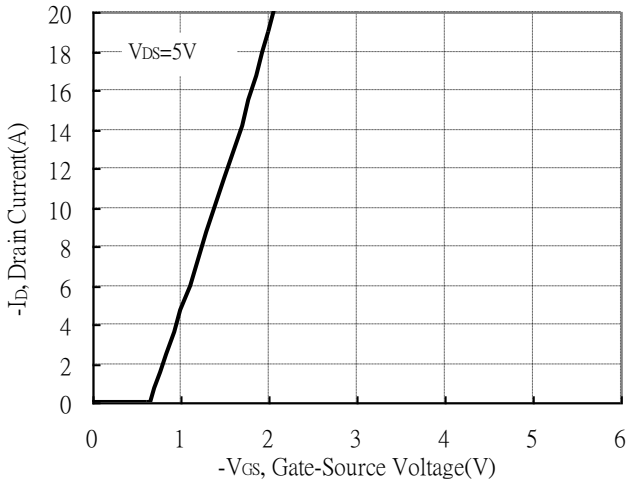


Typical Characteristics(Cont.)

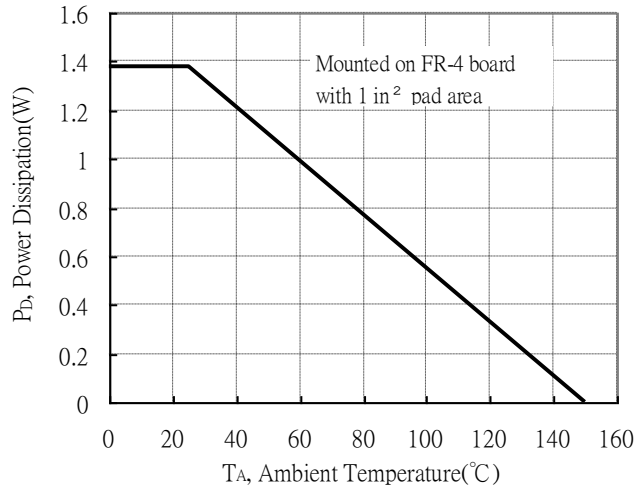


Typical Characteristics(Cont.)

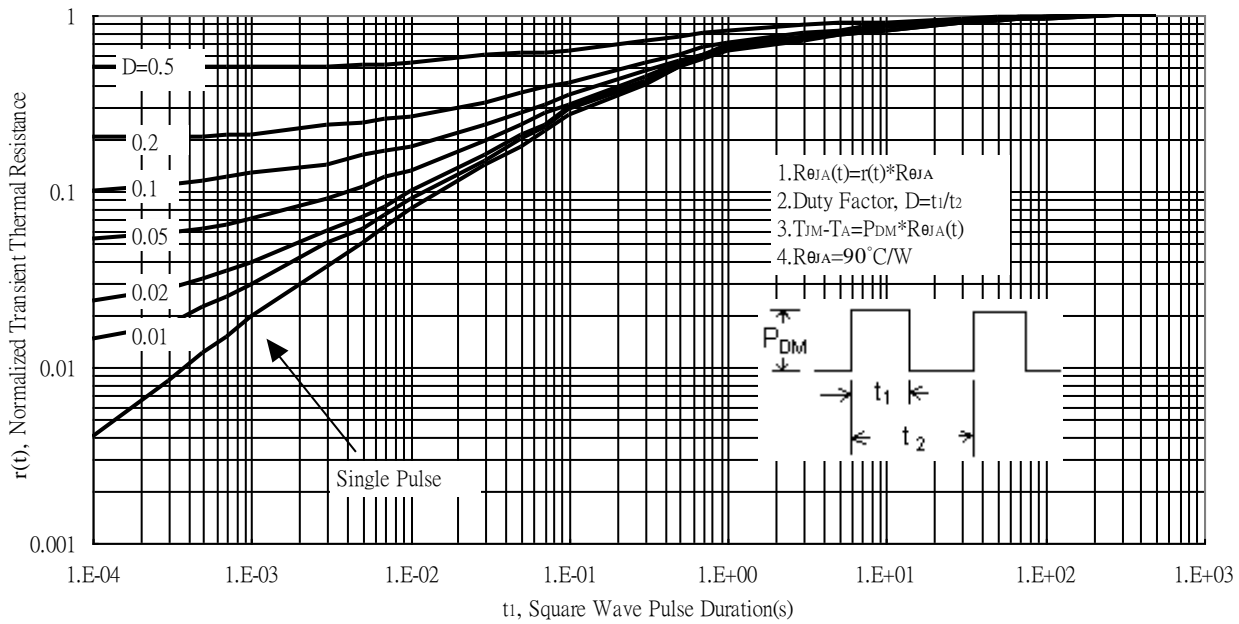
Typical Transfer Characteristics



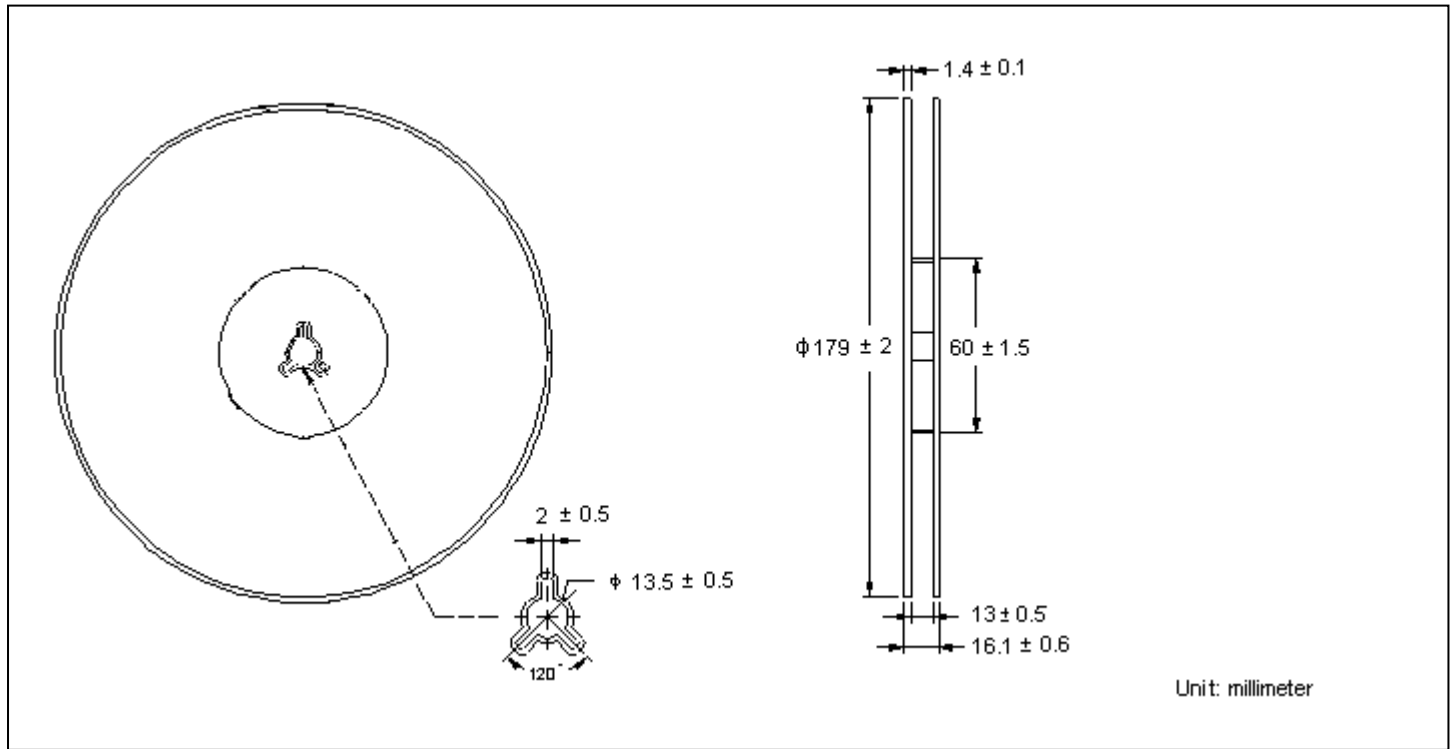
Power Derating Curve



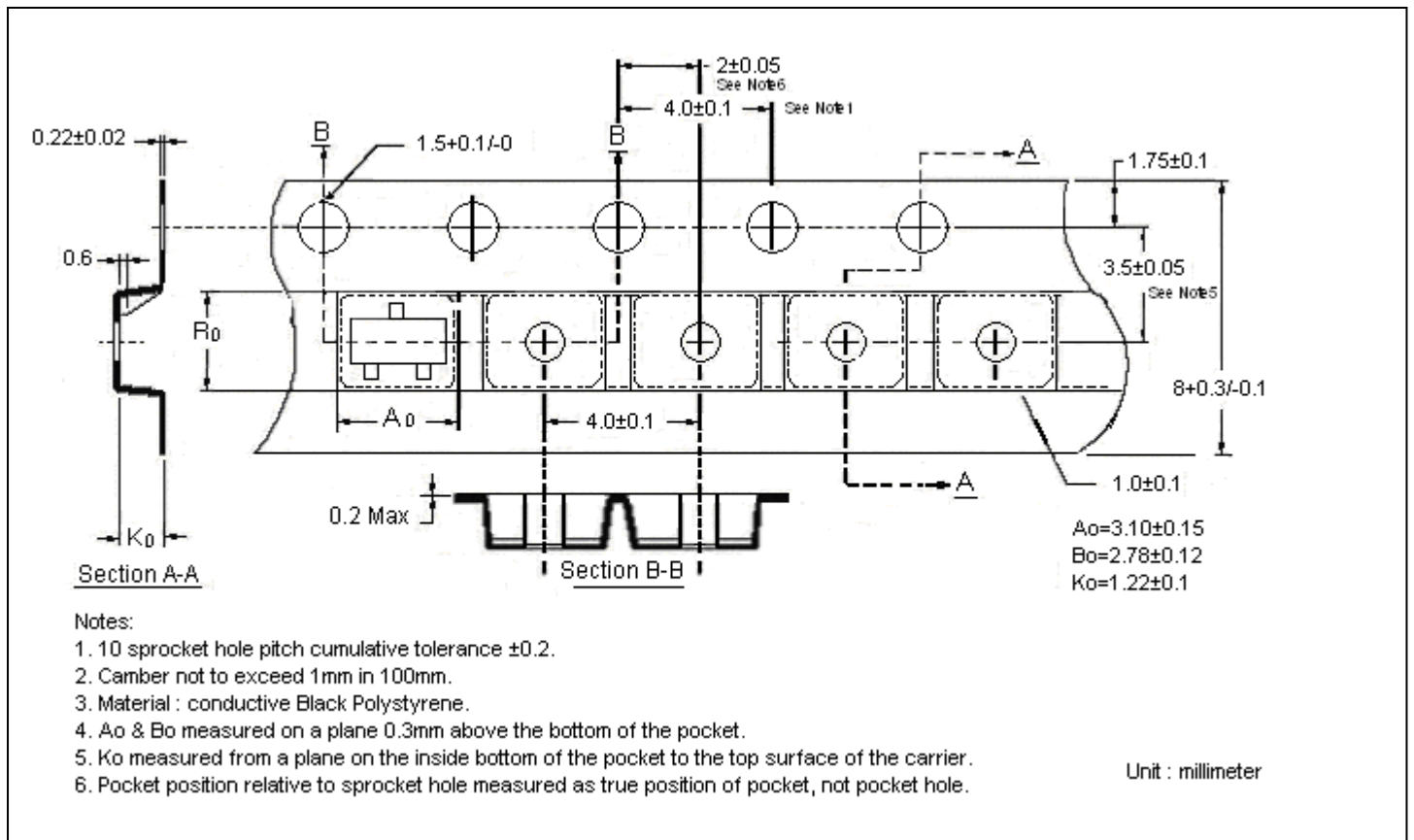
Transient Thermal Response Curves



Reel Dimension



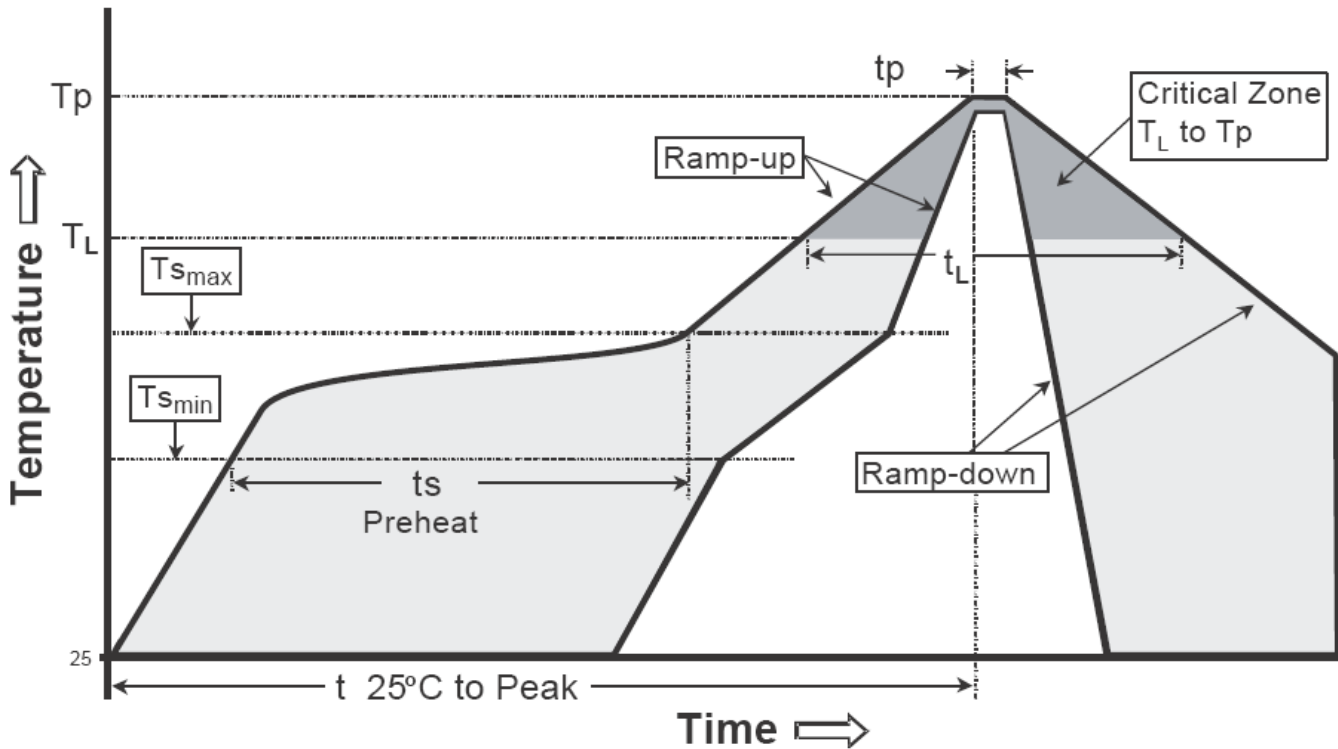
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

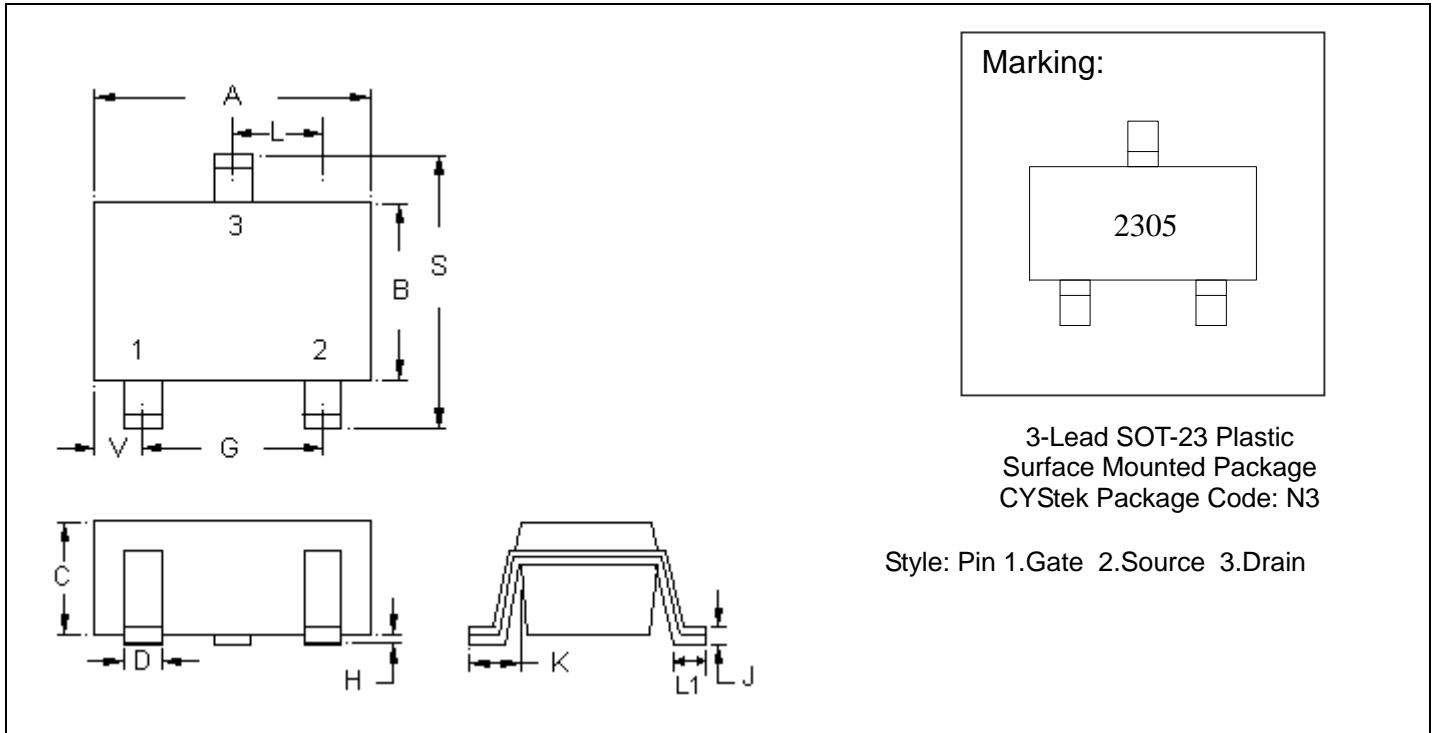
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note :1. All temperatures refer to topside of the package, measured on the package body surface.
 2.For devices mounted on FR-4 PCB of 1.6mm or equivalent grade PCB. If other grade PCB is used, care should be taken to match the coefficients of thermal expansion between components and PCB. If they are not matched well, the solder joints may crack or the bodies of the parts may crack or shatter as the assembly cools.

SOT-23 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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